

X11DPL-I

USER'S MANUAL

Revision 1.1a

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Manual Revision 1.1a

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Preface

About This Manual

This manual is written for system integrators, IT technicians, and knowledgeable end users. It provides information for the installation and use of the X11DPL-i motherboard.

About This Motherboard

The X11DPL-i motherboard supports dual Intel® Xeon Scalable-SP and 2nd Generation Intel Xeon Scalable-SP processors (Socket P) with a TDP (Thermal Design Power) of up to 140W and 2 UPIs (Ultra Path Interconnects) of up to 10.4 GT/s (Note 1 below). Built with the Intel C621 PCH, this motherboard supports up to 2TB of 3DS LRDIMM/LRDIMM/RDIMM/NV-DIMM DDR4 ECC 2933*/2666/2400/2133 MHz memory in 8 memory slots. It offers support for Intel Intelligent Power Node Manager, Active Management Technology, and vPro technology. The X11DPL-i includes ten SATA 3.0 ports, one M.2 slot, and dual LAN and USB 2.0 and 3.0 ports. The X11DPL-i provides maximum performance, cost effectiveness, PCI-E capacity, and I/O flexibility. This motherboard is optimized for PCI-Express expansion with flexible IO support, and is ideal for high-performance server platforms. Please note that this motherboard is intended to be installed and serviced by professional technicians only. For processor/memory updates, please refer to our website at http://www.supermicro.com/products/.



Note: 1. UPI/memory speeds are dependent on the processors installed in your system. **2.** 2933 MHz memory is supported by 2nd Gen Intel Xeon Scalable-SP (82xx/62xx series) processors only.

Manual organization

Chapter 1 describes the features, specifications and performance of the motherboard, and provides detailed information on the Intel C621 chipset.

Chapter 2 provides hardware installation instructions. Read this chapter when installing the processor, memory modules and other hardware components into the system.

Chapter 3 describes troubleshooting procedures for video, memory and system setup stored in the CMOS.

Chapter 4 includes an introduction to the BIOS, and provides detailed information on running the CMOS Setup utility.

Appendix A provides BIOS Error Beep Codes.

Appendix B lists software program installation instructions**.**

Appendix C lists standardized warning statements in various languages**.**

Appendix D contains UEFI BIOS Recovery instructions.

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Table of Contents

Chapter 1 Introduction

1.1 Checklist 8
Quick Reference 11
Quick Reference Table 12
Motherboard Features 14
1.2 Processor and Chipset Overview 18
1.3 Special Features 18
Recovery from AC Power Loss 19
1.4 System Health Monitoring 19
Onboard Voltage Monitors 19
Fan Status Monitor with Firmware Control 19
Environmental Temperature Control 19
System Resource Alert 19
1.5 ACPI Features 20
1.6 Power Supply 20
1.7 Advanced Power Management 20
Management Engine (ME) 20

Chapter 2 Installation

2.1 Static-Sensitive Devices 21
Precautions 21
Unpacking 21
2.2 Motherboard Installation 22
Tools Needed 22
Location of Mounting Holes 22
Installing the Motherboard 23

2.3 Processor and Heatsink Installation....24
Intel Xeon Scalable-SP and 2nd Gen Intel Xeon Scalable-SP Processors....24
Overview of the Processor Socket Assembly....25
Overview of the Processor Heatsink Module (PHM)....26
Preparing the CPU Socket for Installation....27
Removing the Dust Cover from the CPU Socket....27
Attaching the Processor to the CPU/Heatsink Carrier....28
Attaching the CPU/Carrier Assembly to the Passive Heatsink to Form the Processor Heatsink Module (PHM)....29
Installing the Processor Heatsink Module (PHM)....30
Removing the Processor Heatsink Module (PHM)....31
2.4 Memory Support and Installation....32
Memory Support....32
General Memory Population Requirements....32
DDR4 Memory Support for Intel Xeon Scalable-SP Processors....33
DDR4 Memory Support for 2nd Gen Intel Xeon Scalable-SP Processors....33
DIMM Population Guidelines for Optimal Performance....34
DIMM Population Table....35
DIMM Installation....36
DIMM Module Removal....36
2.5 Rear I/O Ports....37
2.6 Front Control Panel....41
2.7 Connectors....46
Power Connector....46
Headers....48
2.8 Jumper Settings....55
How Jumpers Work....55
2.9 LED Indicators....58
2.10 PCI-E 3.0 Slots....61

Chapter 3 Troubleshooting

3.1 Troubleshooting Procedures 62

Before Power On 62

No Power 62

No Video 63

System Boot Failure 63

Memory Errors 63

Losing the System's Setup Configuration 64

When the System Becomes Unstable 64

3.2 Technical Support Procedures 66

3.3 Frequently Asked Questions 67

3.4 Battery Removal and Installation 68

Battery Removal 68

Proper Battery Disposal 68

Battery Installation 68

3.5 Returning Merchandise for Service 69

Chapter 4 UEFI BIOS

4.1 Introduction 70

4.2 Main Setup 71

4.3 Advanced Setup Configurations 73

4.4 Event Logs 104

4.5 IPMI 106

4.7 Boot 112

4.8 Save & Exit 114

Appendix A BIOS Codes

Appendix B Software Installation

B.1 Installing Software Programs 118

B.2 SuperDoctor® 5 119

Appendix C Standardized Warning Statements

Battery Handling 120

Product Disposal 122

Appendix D UEFI BIOS Recovery

Chapter 1

Introduction

Congratulations on purchasing your computer motherboard from an industry leader. Supermicro motherboards are designed to provide you with the highest standards in quality and performance.

In addition to the motherboard, several important parts that are included with your shipment are listed below. If anything listed is damaged or missing, please contact your retailer.

1.1 Checklist

Main Parts List		
Description	**Part Number**	**Quantity**
Supermicro Motherboard	X11DPL-i	1
SATA Cables	CBL-0044L	6
I/O Shield	MCP-260-00110-0N	1

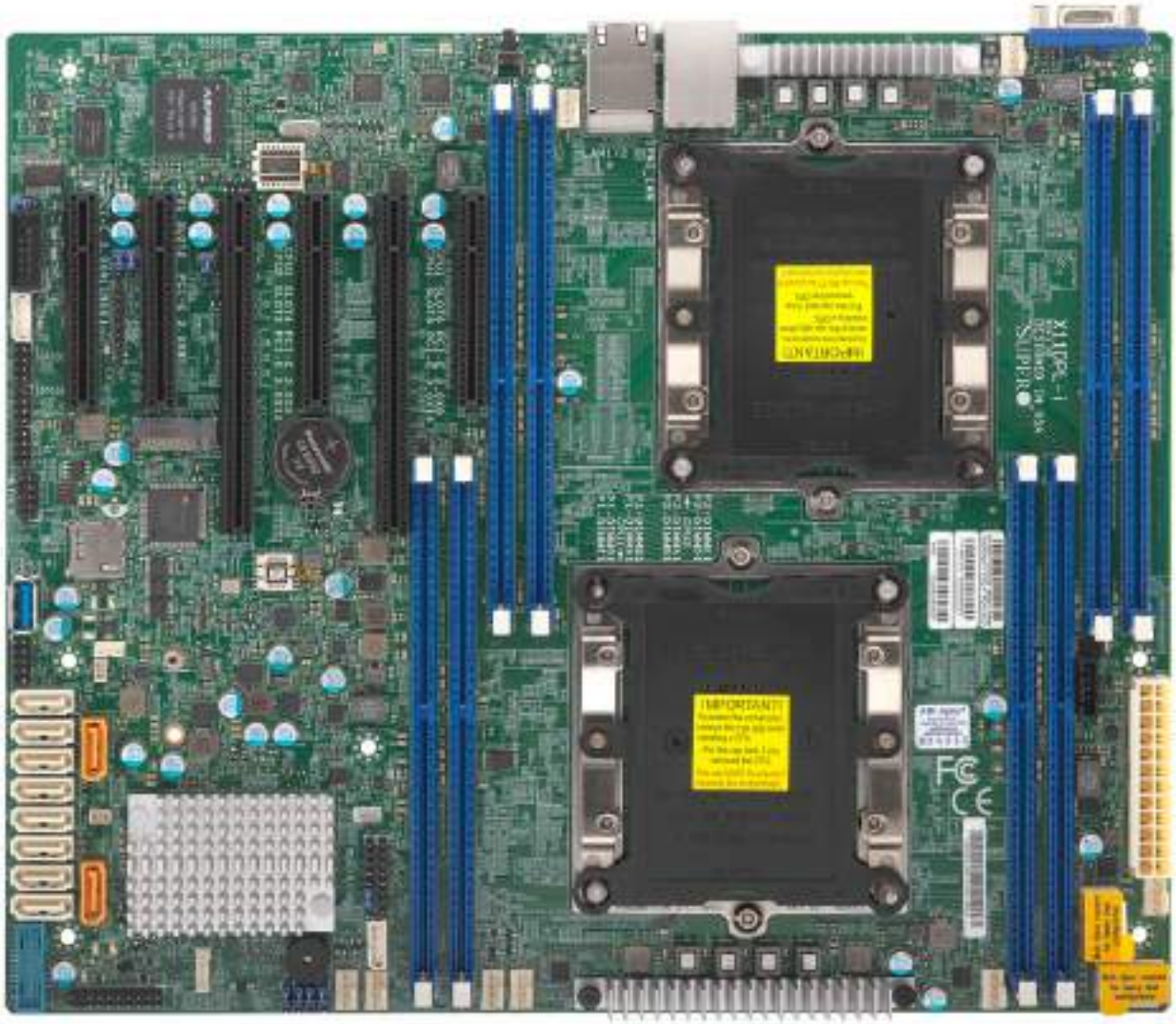
Important Links

For your system to work properly, please follow the links below to download all necessary drivers/utilities and the user's manual for your server.

- Supermicro product manuals: http://www.supermicro.com/support/manuals/
- Product drivers and utilities: http://www.supermicro.com/wftp
- Product safety info: http://www.supermicro.com/about/policies/safety_information.cfm
- If you have any questions, please contact our support team at: support@supermicro.com

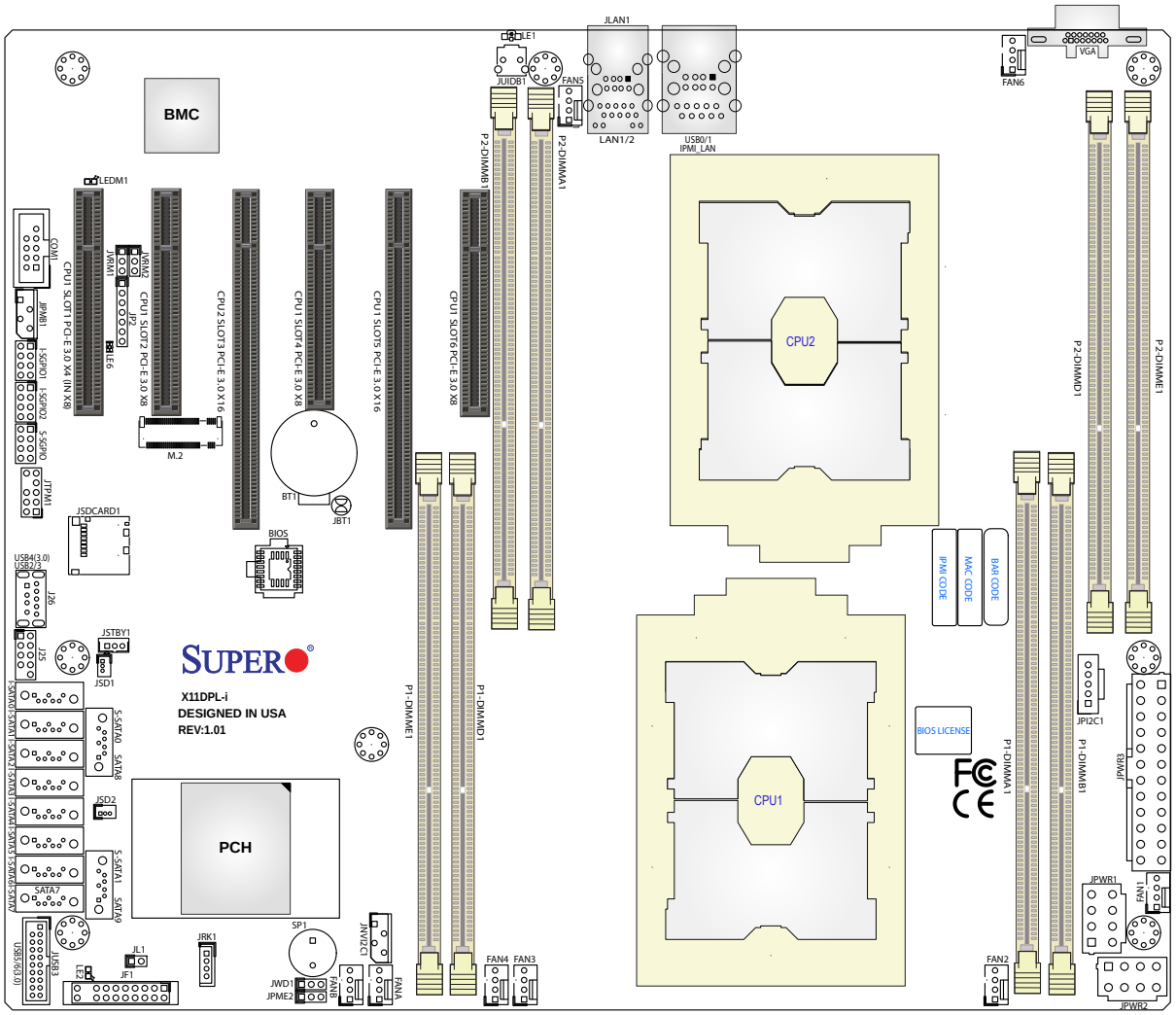
This manual may be periodically updated without notice. Please check the Supermicro website for possible updates to the manual revision level.

Figure 1-1. Motherboard Image



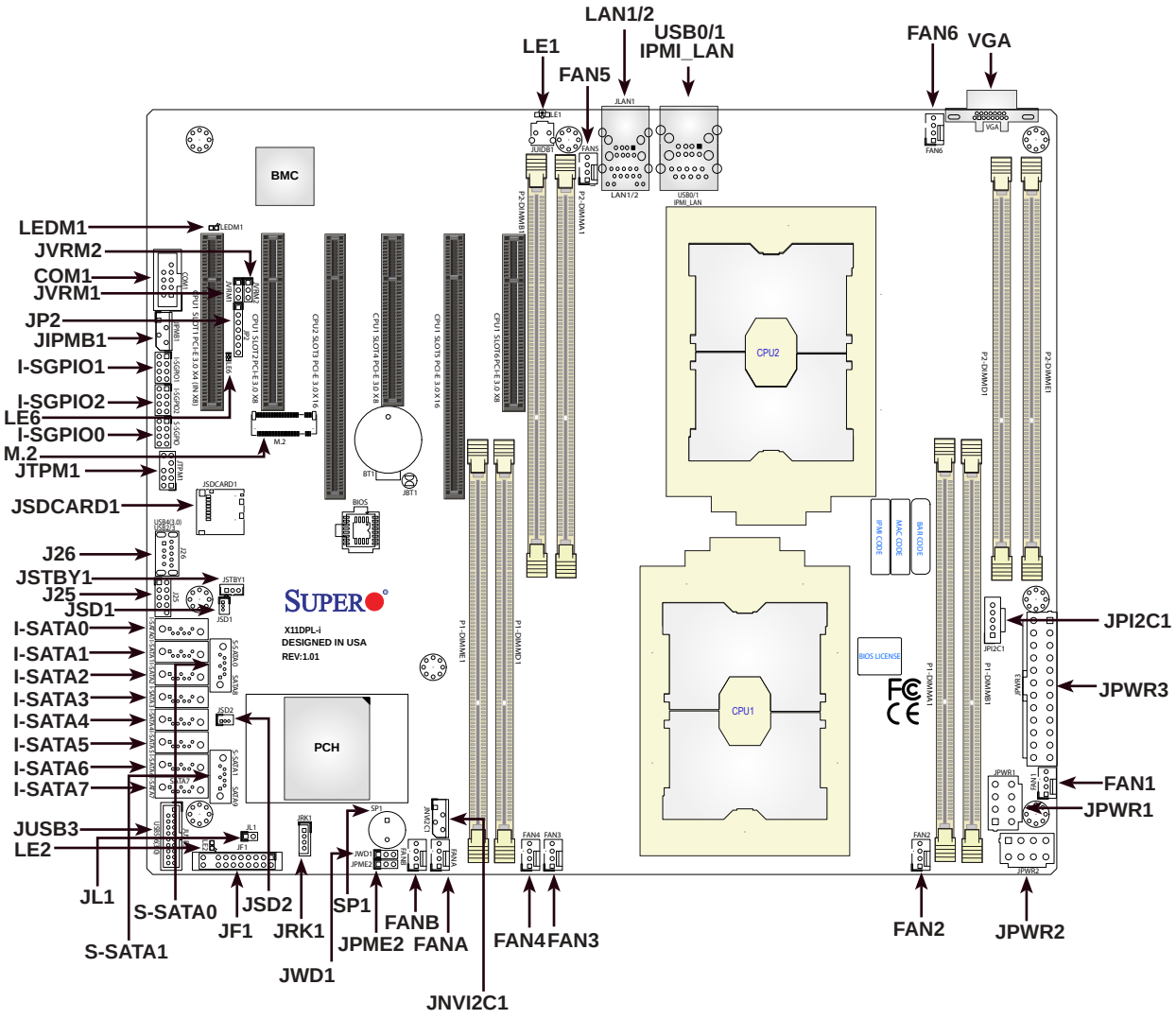
Note: All graphics shown in this manual were based upon the latest PCB revision available at the time of publication of the manual. The motherboard you received may or may not look exactly the same as the graphics shown in this manual.

Figure 1-2. X11DPL-i Motherboard Layout
(not drawn to scale)



Note: Components not documented are for internal testing only.

Quick Reference



Notes:

- See Chapter 2 for detailed information on jumpers, I/O ports, and JF1 front panel connections.
- "■" indicates the location of Pin 1.
- Jumpers/LED indicators not indicated are used for internal testing only.
- Use only the correct type of onboard CMOS battery as specified by the manufacturer. Do not install the onboard battery upside down to avoid possible explosion.

Quick Reference Table

Jumper	Description	Default Setting
JBT1	Clear CMOS	Open (Normal)
JPME2	Manufacturer (ME) Mode Select	Pins 1-2 (Normal)
JVRM1	VRM SMB Clock (to BMC or PCH)	Pins 1-2 BMC (Normal)
JVRM2	VRM SMB Data (to BMC or PCH)	Pins 1-2 BMC (Normal)
JWD1	Watch-Dog Timer Enable	Pins 1-2 (Reset)

Connector	Description
Battery (BT1)	Onboard CMOS battery
COM 1	Front Panel COM port 1
FAN1 ~ FAN6	System cooling fan headers
FANA/FANB	Thermal fan headers for I/O add-on cards
IPMI_LAN	Dedicated IPMI LAN port
(I-)SATA0~3, 4~7	I-SATA 3.0 connectors supported by the Intel PCH
J25	USB 2.0 Header for USB ports 2/3
J26	USB 3.0 Type A port for USB4
JF1	Front Control Panel header
JIPMB1	4-pin external BMC I²C header (for an IPMI card)
JL1	Chassis intrusion header (**Note:** please connect a cable from the Chassis Intrusion header at JL1 to the chassis to receive an alert via IPMI.)
JNVI²C1	NVMe SMBus (I²C) header used for PCI-E hot-plug SMBus clock & data connections (an SMCI-proprietary NVMe add-on card and cable are required; available for a Supermicro complete system only)
JPI²C1	Power Supply I²C connector
JPWR1/2	8-pin Power connectors
JPWR3	24-pin Power connectors
JRK1	RAID key for CPU NVME SSD
JSD1/JSD2	Power connector SATA DOM
JSDCARD1	Micro SD Card slot
JSTBY1	Wake On LAN header
JTPM1	Trusted Platform Module (TPM)/Port 80 connector
M.2	PCI-E M.2 slot
(S-)SATA0/1	S-SATA 3.0 connectors supported by the Intel PCH
SP1	Internal speaker
USB0/USB1	Back panel USB 2.0 ports
VGA	Back panel VGA port

LED	Description	Status
LE1	UID (Unit Identifier) LED	Solid Blue: Unit Identified
LE6	Onboard Power LED	Solid Red: Standby Solid Green: Power On
LEDM1	BMC Heartbeat LED	Blinking Green: BMC Normal

Note: To avoid causing interference with other components, please be sure to use an add-on card that is fully compliant with the PCI-E standard on a PCI-E slot.

Motherboard Features

Motherboard Features	
CPU	
• This motherboard supports dual Intel Xeon Scalable-SP and 2nd Gen Intel Xeon Scalable-SP processors which offer two Intel® UltraPath Interconnect (UPI) links of up to 10.4 GT/s **Note:** Both CPUs need to be installed for full access to the PCI-E slots, DIMM slots, and onboard controllers. Refer to the block diagram on page 16 to determine which slots or devices may be affected.	
Memory	
• Integrated memory controller supports up to 2TB of 3DS LRDIMM/LRDIMM/RDIMM/NV-DIMM DDR4 ECC 2933/2666/2400/2133 MHz memory in 8 memory slots **Note:** 2933 MHz memory is supported by 2nd Gen Intel Xeon Scalable-SP (82xx/62xx) series processors only.	
DIMM Size	
• Up to 128GB at 1.2V **Note 1:** Memory speed support depends on the processors used in the system. **Note 2:** For the latest CPU/memory updates, please refer to our website at http://www.supermicro.com/products/motherboard.	
Chipset	
• Intel C621	
Expansion Slots	
• Two (2) PCI-Express 3.0 x16 slots supported by CPU1 and CPU2 • Three (3) PCI-Express 3.0 x8 slots supported by CPU1 • One (1) PCI-Express 3.0 x4 in x8 supported by CPU1 • M.2 Interface: 1 SATA/PCI-E 3.0 x4 • M.2 Form Factor: 2260, 2280 • M.2 Key: M-Key	
BaseBoard Management Controller (BMC)	
• ASPEED AST2500 Baseboard Management Controller (BMC) supports IPMI 2.0 • One (1) dedicated IPMI LAN located on the rear IO backpanel	
Graphics	
• Graphics controller via AST2500 BMC (BaseBoard Management Controller)	
Network	
• Dual LAN with Marvell 88E1512 Gigabit Ethernet PHY	
I/O Devices	
• SATA 3.0	Ten (8 SATA + 2 SATA DOM) SATA ports • I-SATA0~7 • S-SATA0/1
• RAID (PCH)	• RAID 0, 1, 5, 10
Peripheral Devices	
• Two (2) USB 2.0 ports on the rear I/O panel (USB 0/1) • One (1) internal USB 2.0 header for two (2) USB front panel support (USB 2/3) • One (1) Type A USB 3.0 connector (USB 4) • One (1) internal USB 3.0 header for two (2) USB front panel support (USB 5/6)	

Motherboard Features
BIOS
• 256Mb AMI BIOS SPI Flash ROM • ACPI 6.0 or later, SPI dual/quad speed support, and SMBIOS 3.1.1 or later • Riser Card auto detection support
Power Management
• ACPI power management • Power button override mechanism • Power-on mode for AC power recovery • Intel® Intelligent Power Node Manager 3.0 (available when the Supermicro Power Manager [SPM] is installed and a special power supply is used. See the note on page 20.) • Management Engine (ME)
System Health Monitoring
• Onboard voltage monitoring for +1.8V, +3.3V, +5V, +12V, +3.3V standby, +5V standby, VBAT, CPU Core, memory, PCH temperature, system temperature, and memory temperature • CPU 4+1 phase switching voltage regulator • CPU thermal trip support • Status monitor for on/off control • CPU Thermal Design Power (TDP) support of up to 140W (See Note 1 on next page.)
Fan Control
• Eight 4-pin fan headers • Fan status monitoring via IPMI • Multi-speed fan control via onboard BMC
System Management
• Trusted Platform Module (TPM) 2.0 • PECI (Platform Environment Control Interface)/ TSI 2.0 support • SuperDoctor® 5, Watch Dog, Non-maskable interrupt (NMI), RoHS • IPMIView, SMCIPMITOOL, IPMICFG • Chassis intrusion header and detection (**Note:** Please connect a cable from the Chassis Intrusion header at JL1 to the chassis to receive an alert via IPMI.)
LED Indicators
• CPU/Overheating • Power/Suspend-state indicator • Fan failure • UID/remote UID • HDD activity • LAN activity
Dimensions
• 12.07" (L) x 10.15" (W) (306.58 mm x 257.81 mm)



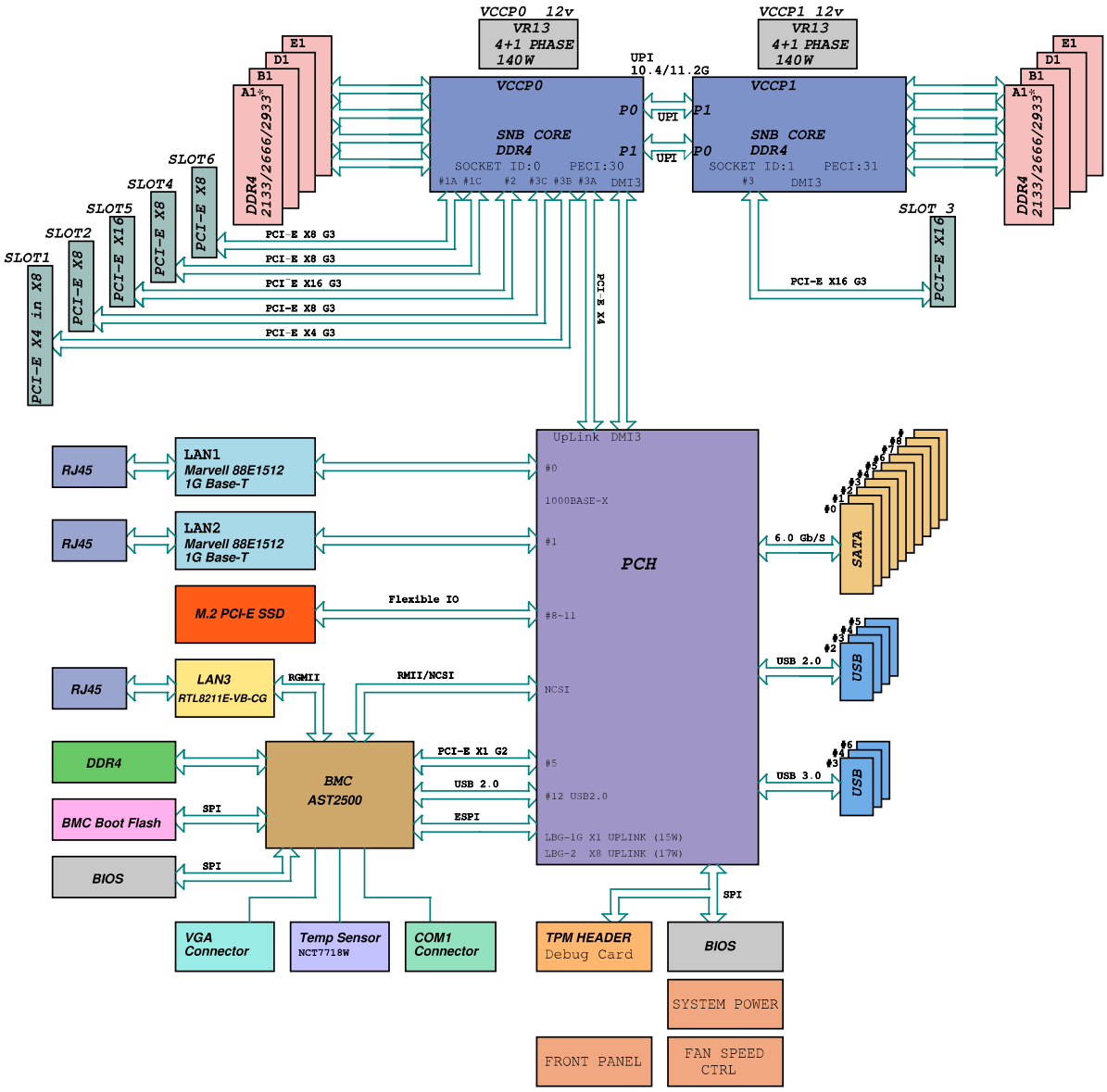
Note: The table above is continued on the next page.

Note 1: The CPU maximum thermal design power (TDP) is subject to chassis and heatsink cooling restrictions. For proper thermal management, please check the chassis and heatsink specifications for proper CPU TDP sizing.

Note 2: For IPMI configuration instructions, please refer to the Embedded IPMI Configuration User's Guide available at http://www.supermicro.com/support/manuals/.

Note 3: It is strongly recommended that you change BMC login information upon initial system power-on. The manufacturer default username is ADMIN and the password is ADMIN. For proper BMC configuration, please refer to http://www.supermicro.com.

Figure 1-3.
System Block Diagram



Note 1: 2933 MHz memory is supported by 2nd Gen Intel Xeon Scalable-SP (82xx/62xx) series processors only.

Note 2: This is a general block diagram and may not exactly represent the features on your motherboard. See the previous pages for the actual specifications of your motherboard.

1.2 Processor and Chipset Overview

Built upon the functionality and capability of Intel Xeon Scalable-SP or 2nd Gen Intel Xeon Scalable-SP processors (Socket P) with support of C621 chipset, the X11DPL-i motherboard provides a cost effective design, power efficiency, and maximum PCI-E capacity. With M.2 support and six PCI-E slots in a small DP Form Factor, the X11DPL-i combines high-end performance, excellent value, and the latest in SSD and SATA support. This motherboard is optimized for high performance server platforms.

Features Supported by Intel Xeon Scalable-SP Series Processors

Intel Xeon Scalable-SP processors support the following features:

- Intel AVX-512 instruction support to handle complex workloads
- Hot plug and enclosure management with Intel Volume Management Device (Intel VMD)
- Rich set of available IOs with increased PCI-E lanes (48 lanes)

New features supported by 2nd Gen Intel Xeon Scalable-SP Series Processors

2nd Gen Intel Xeon Scalable-SP processors support the following features:

- Higher performance for a wider range of workloads with per-core performance increase
- Up to 2933 MHz memory supported
- Vector Neural Network Instruction (VNNI) support for Accelerate Deep Learning & Arti-fi cial Intelligence (AI) workloads
- Speed Select Technology provides multiple CPU profiles that can be set in the BIOS.(This feature is available on select CPU SKUs).
- Seamless hardware security mitigations & performance/frequency flexibility

Note: 2933 MHz memory are supported by 2nd Gen Intel Xeon Scalable-SP (82xx/62xx series) processors only.

1.3 Special Features

This section describes the health monitoring features of the motherboard. The motherboard has an onboard ASPEED AST2500 Baseboard Management Controller (BMC) that supports system health monitoring.

Recovery from AC Power Loss

The Basic I/O System (BIOS) provides a setting that determines how the system will respond when AC power is lost and then restored to the system. You can choose for the system to remain powered off (in which case you must press the power switch to turn it back on), or for it to automatically return to the power-on state. See the Advanced BIOS Setup section for this setting. The default setting is Last State.

1.4 System Health Monitoring

This section describes the health monitoring features of the X11DPL-i motherboard. The motherboard has an onboard Baseboard Management Controller (BMC) chip that supports system health monitoring.

Onboard Voltage Monitors

The onboard voltage monitor will continuously scan crucial voltage levels. Once a voltage becomes unstable, it will give a warning or send an error message to the IPMI WebGUI and IPMIView. Real time readings of these voltage levels are all displayed in IPMI.

Fan Status Monitor with Firmware Control

The system health monitor embedded in the BMC chip can check the RPM status of the cooling fans. The CPU and chassis fans are controlled via IPMI.

Environmental Temperature Control

System Health sensors in the BMC monitor the temperatures and voltage settings of onboard processors and the system in real time via the IPMI interface. Whenever the temperature of the CPU or the system exceeds a manufacturer-define threshold, system/CPU cooling fans will be turned on to prevent the CPU or the system from overheating.

Note: To avoid possible system overheating, please be sure to provide adequate airflow to your system.

System Resource Alert

This feature is available when used with SuperDoctor 5®. SuperDoctor 5 is used to notify the user of certain system events. For example, you can configure SuperDoctor 5 to provide you with warnings when the system temperature, CPU temperatures, voltages and fan speeds go beyond a predefined range.

1.5 ACPI Features

ACPI stands for Advanced Configuration and Power Interface. The ACPI specification defines a flexible and abstract hardware interface that provides a standard way to integrate power management features throughout a computer system including its hardware, operating system and application software. This enables the system to automatically turn on and off peripherals such as network cards, hard disk drives and printers.

In addition to enabling operating system-directed power management, ACPI also provides a generic system event mechanism for Plug and Play and an operating system-independent interface for configuration control. ACPI leverages the Plug and Play BIOS data structures while providing a processor architecture-independent implementation that is compatible with appropriate Windows operating systems. For detailed information on OS support, please refer to our website at www.supermicro.com.

1.6 Power Supply

As with all computer products, a stable power source is necessary for proper and reliable operation. It is even more important for processors that have high CPU clock rates and in areas where noisy power transmission is present.

1.7 Advanced Power Management

The following new advanced power management features are supported by the motherboard.

Management Engine (ME)

The Management Engine, which is an ARC controller embedded in the IOH (I/O Hub), provides Server Platform Services (SPS) to your system. The services provided by SPS are different from those provided by the ME on client platforms.

Chapter 2

Installation

2.1 Static-Sensitive Devices

Electrostatic Discharge (ESD) can damage electronic components. To avoid damaging your motherboard and your system, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

Precautions

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing the board from the antistatic bag.
- Handle the board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the motherboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure that your chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the motherboard.
- Use only the correct type of CMOS onboard battery as specified by the manufacturer. Do not install the CMOS battery upside down, which may result in a possible explosion.

Unpacking

The motherboard is shipped in antistatic packaging to avoid static damage. When unpacking the motherboard, make sure that the person handling it is static protected.

2.2 Motherboard Installation

All motherboards have standard mounting holes to fit different types of chassis. Make sure that the locations of all the mounting holes for both the motherboard and the chassis match. Although a chassis may have both plastic and metal mounting fasteners, metal ones are highly recommended because they ground the motherboard to the chassis. Make sure that the metal standoffs click in or are screwed in tightly.



Phillips Screwdriver (1)

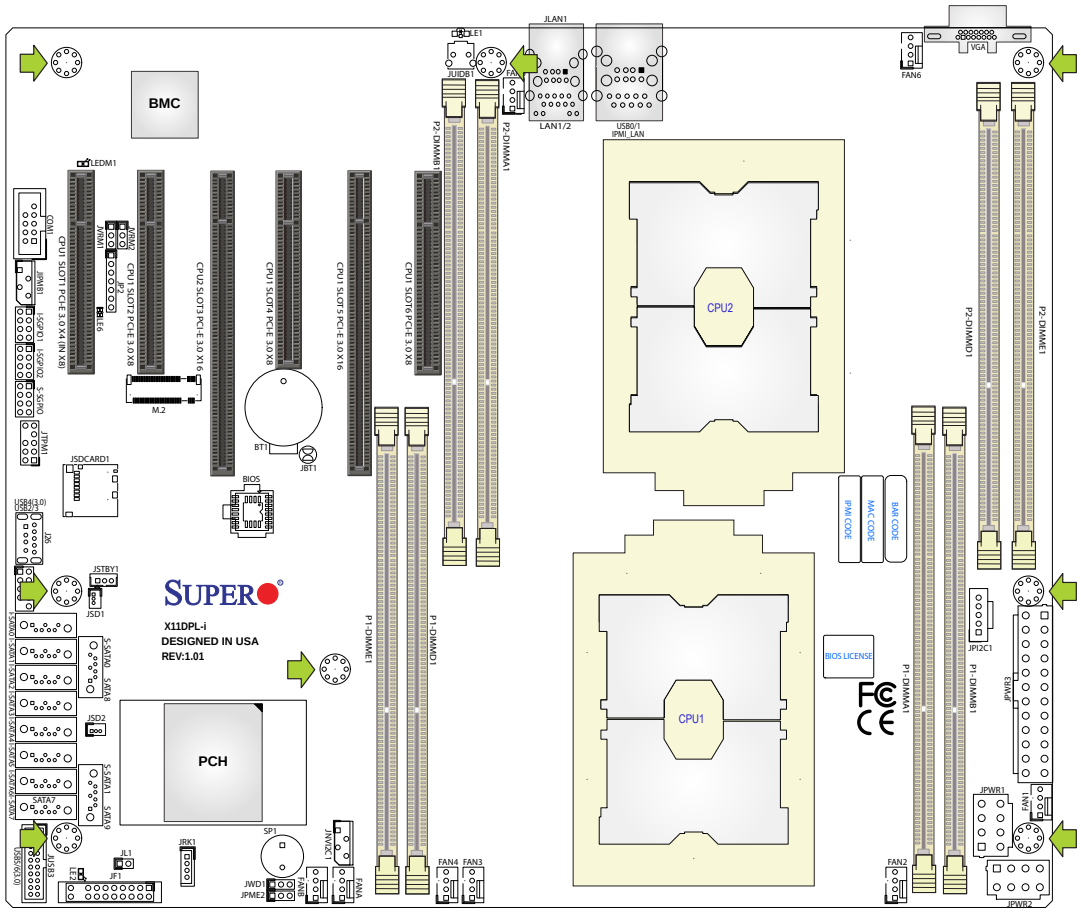


Phillips Screws (8)



Standoffs (8)

Tools Needed

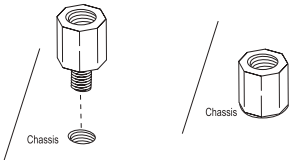


Location of Mounting Holes

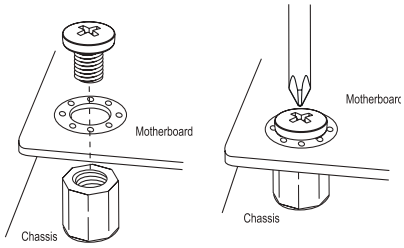
Notes: 1. To avoid damaging the motherboard and its components, please do not use a force greater than 8 lb/inch on each mounting screw during motherboard installation. **2.** Some components are very close to the mounting holes. Please take precautionary measures to avoid damaging these components when installing the motherboard to the chassis. **3.** to avoid causing interference with other components, please be sure to use an add-on card that is fully compliant with the PCI-standard on a PCI slot.

Installing the Motherboard

1. Install the I/O shield into the back of the chassis if needed.
2. Locate the mounting holes on the motherboard. See the previous page for the location.



3. Locate the matching mounting holes on the chassis. Align the mounting holes on the motherboard against the mounting holes on the chassis.



4. Install standoffs in the chassis as needed.
5. Install the motherboard into the chassis carefully to avoid damaging other motherboard components.
6. Using the Phillips screwdriver, insert a Phillips head #6 screw into a mounting hole on the motherboard and its matching mounting hole on the chassis.
7. Repeat Step 5 to insert #6 screws into all mounting holes.
8. Make sure that the motherboard is securely placed in the chassis.

Note: Images displayed in this manual are for illustration only. Your chassis or components might look different from those shown in this manual.

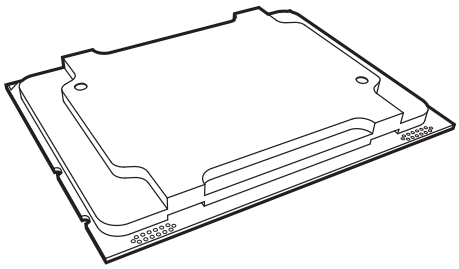
2.3 Processor and Heatsink Installation

Warning: When handling the processor package, avoid placing direct pressure on the label area of the fan. Also, improper CPU installation or socket misalignment can cause serious damage to the CPU or the motherboard that will require RMA repairs. Please read and follow all instructions thoroughly before installing your CPU and heatsink.

Notes:

- Always connect the power cord last, and always remove it before adding, removing or changing any hardware components. Make sure that you install the processor into the CPU socket before you install the CPU heatsink.
- If you buy a CPU separately, make sure that you use an Intel-certified multi-directional heatsink only.
- Make sure to install the motherboard into the chassis before you install the CPU heatsink.
- When receiving a motherboard without a processor pre-installed, make sure that the plastic CPU socket cap is in place and none of the socket pins are bent; otherwise, contact your retailer immediately.
- Refer to the Supermicro website for updates on CPU support.

Intel Xeon Scalable-SP and 2nd Gen Intel Xeon Scalable-SP Processors



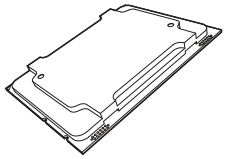
Intel Xeon Scalable-SP and 2nd Gen Intel Xeon Scalable-SP Processor

Note: All graphics, drawings and pictures shown in this manual are for illustration only. The components that came with your machine may or may not look exactly the same as those shown in this manual.

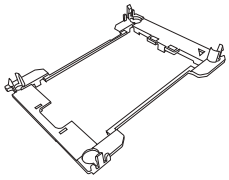
Overview of the Processor Socket Assembly

The processor socket assembly contains 1) Intel Xeon Scalable-SP or 2nd Gen Intel Xeon Scalable-SP processor, 2) CPU/heatsink carrier, 3) dust cover, and 4) CPU socket.

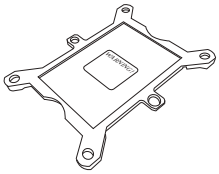
1. Intel processors



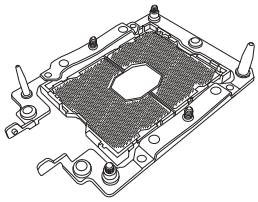
2. CPU/Heatsink Carrier



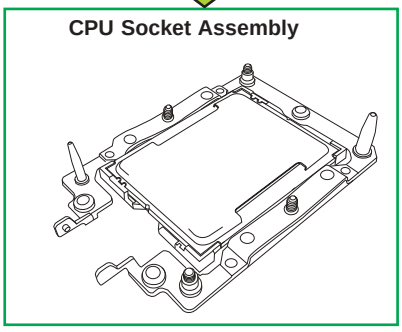
3. Dust Cover



4. CPU Socket





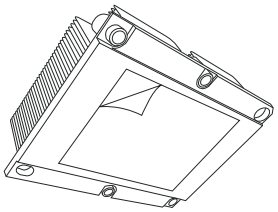


Note: Be sure to cover the CPU socket with the dust cover when the CPU is not installed.

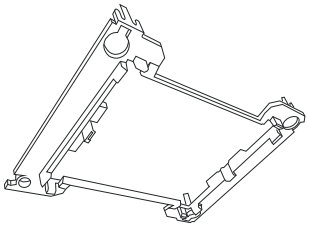
Overview of the Processor Heatsink Module (PHM)

The processor heatsink module (PHM) contains 1) a passive heatsink, 2) a CPU/heatsink carrier, and 3) Intel Xeon Scalable-SP or 2nd Gen Intel Xeon Scalable-SP processor.

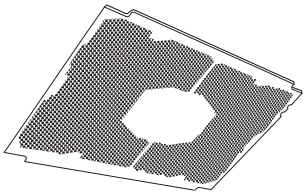
1. Passive Heatsink



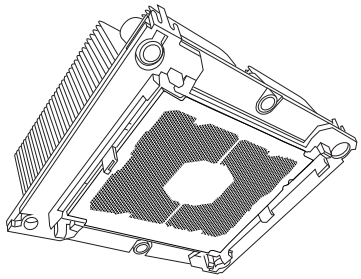
2. CPU/Heatsink Carrier



3. Intel Processor



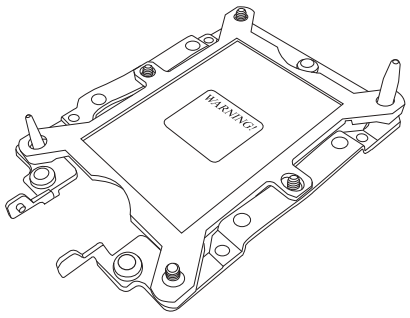
Processor Heatsink Module (PHM)



(Bottom View)

Preparing the CPU Socket for Installation

This motherboard comes with the CPU socket pre-assembled in the factory. The CPU socket contains 1) a dust cover, 2) a socket bracket, 3) the CPU socket, and 4) a back plate. These components are pre-installed on the motherboard before shipping.

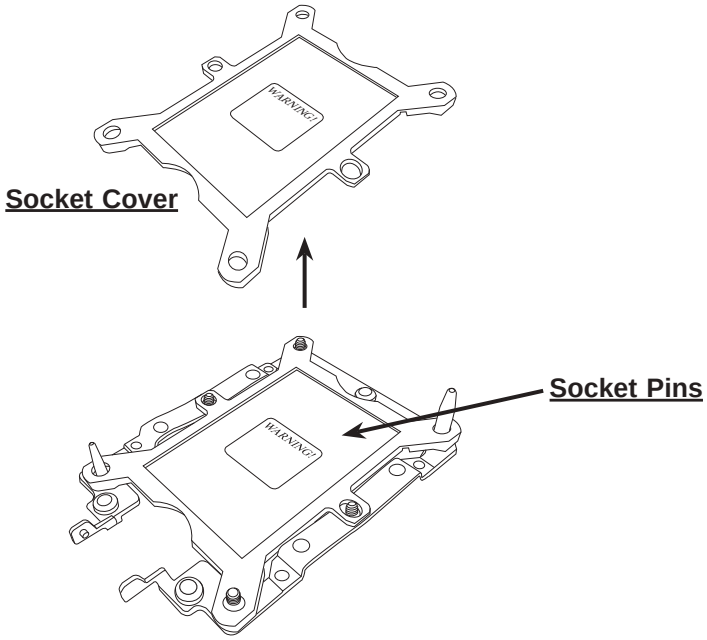


Processor Socket Assembly

Removing the Dust Cover from the CPU Socket

Remove the dust cover from the CPU socket, exposing the CPU socket and socket pins as shown on the illustration below.

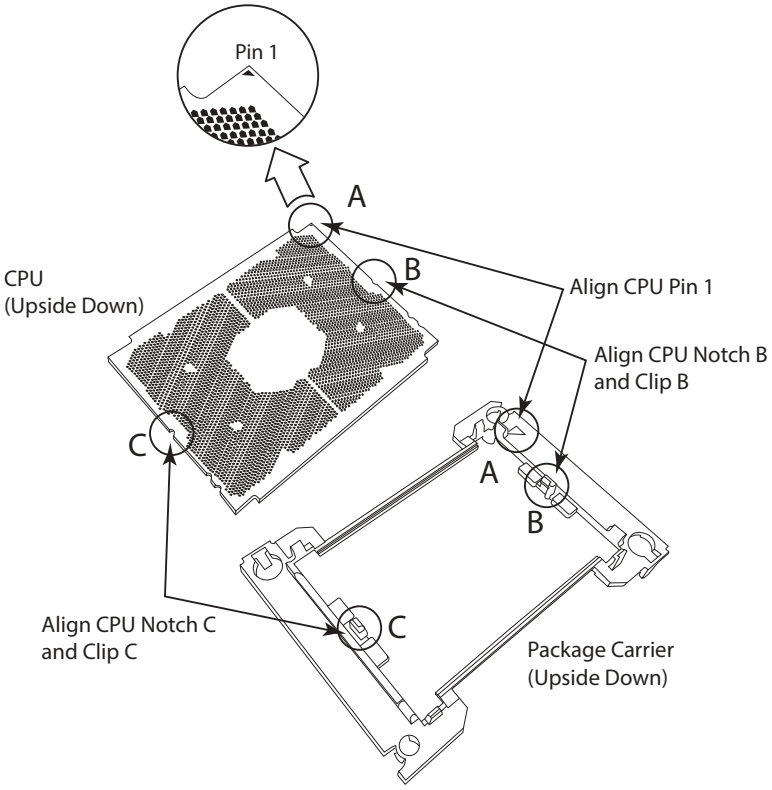
Note: Do not touch the socket pins to avoid damaging them, causing the CPU to malfunction.

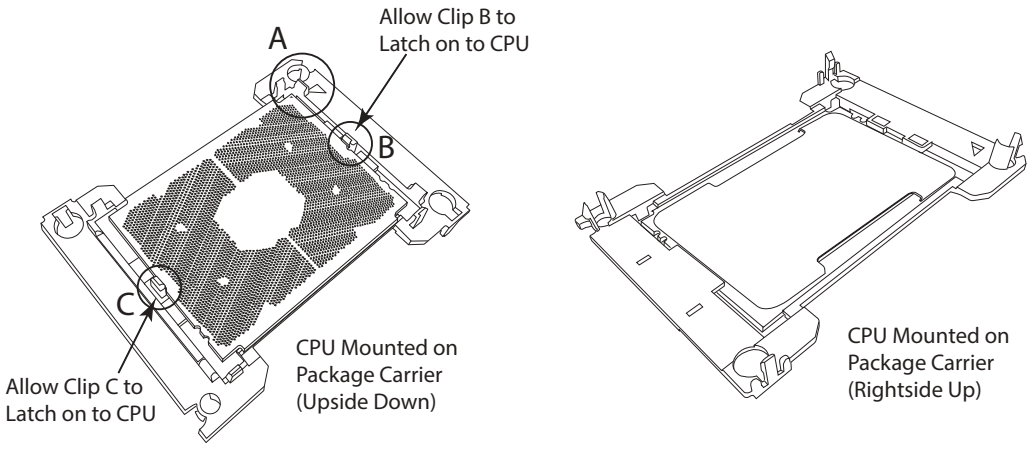


Attaching the Processor to the CPU/Heatsink Carrier

To properly install the CPU onto the CPU/heatsink carrier, please follow the steps below.

1. Locate Pin 1 (Notch A), Notch B, and Notch C on the CPU and locate Pin 1 (Notch A), Notch B, and Notch C on the CPU/heatsink carrier.
2. Align Pin 1 (Notch A), Notch B, and Notch C on the CPU with the corresponding notches on the carrier. Once they are aligned, carefully insert the CPU into the carrier until you hear a click. Once the CPU is properly mounted onto the carrier, the CPU/carrier assembly is made.





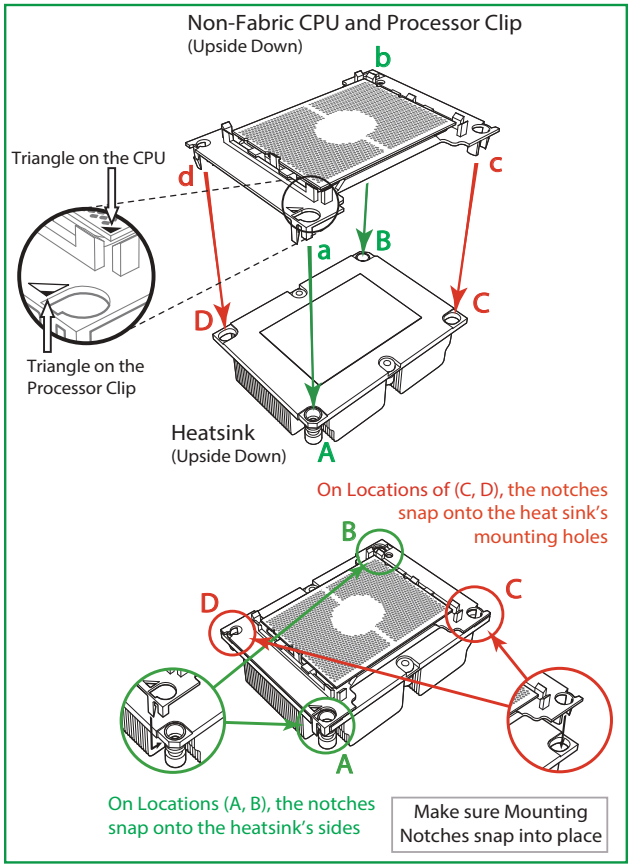
Attaching the CPU/Carrier Assembly to the Passive Heatsink to Form the Processor Heatsink Module (PHM)

After you have made a CPU/carrier assembly, please follow the steps below to mount the assembly onto the heatsink to create the Processor Heatsink Module (PHM).

1. Place the heatsink upside down with the thermal grease facing up. Locate two larger mounting holes (A, B) at the diagonal corners of the heatsink, and two smaller mounting holes (C, D) on the heatsink.

2. Hold the CPU/carrier at the center edge, and turn it upside down with the CPU pins facing up. Locate the two larger holes (1, 2) at the diagonal corners of the carrier and the smaller holes of the same size (3, 4) on the carrier. Please note the mounting clips located next to every mounting hole on the carrier.

3. Align the larger holes (1, 2) on the carrier against the larger mounting holes (A, B) on the heatsink and smaller holes (3, 4) on the carrier against the smaller mounting holes (C, D) on the heatsink. Insert the mounting clips next to the larger hole on the carrier into the larger mounting hole on the heatsink (1➔A, 2➔B) and snap the mounting clips next to the smaller holes on the carrier onto the edges of the heatsink next to the smaller holes (3➔C, 4➔D) making sure that the mounting clips snap into place, and that the CPU/carrier assembly is properly mounted onto the heatsink. By mounting the CPU/carrier assembly to the heatsink, the Processor Heatsink Module (PHM) is assembled.



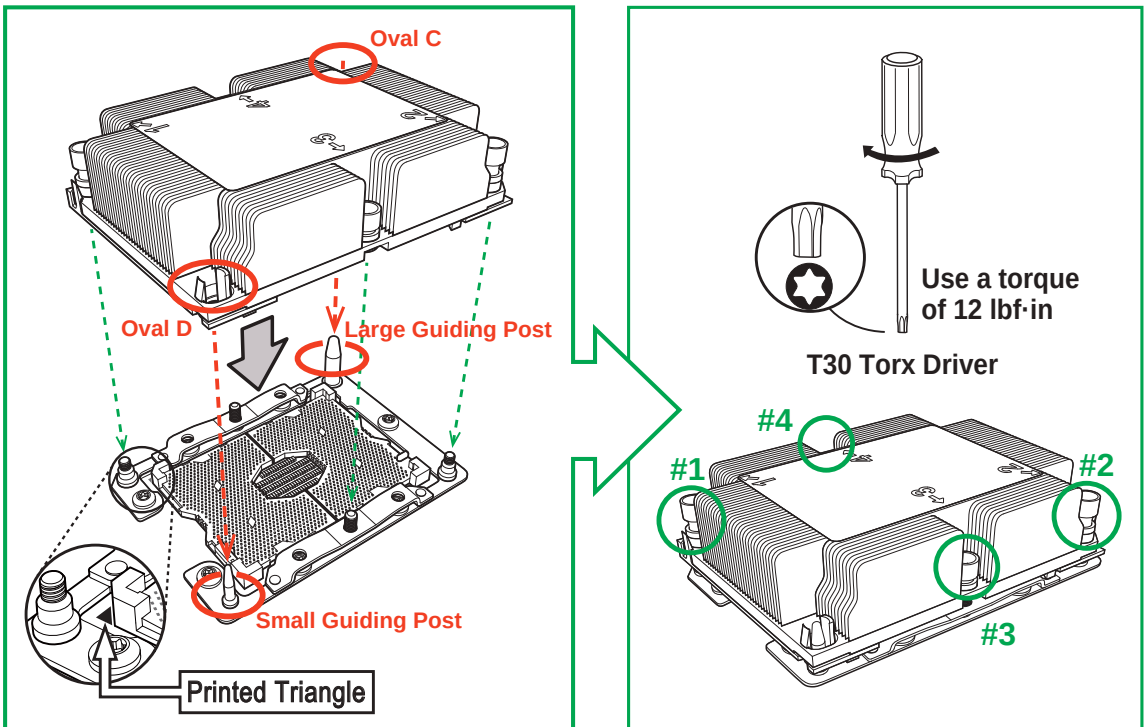
Installing the Processor Heatsink Module (PHM)

1. Once you have assembled the processor heatsink module (PHM) by following the instructions listed on the previous page, align the processor heatsink module with the CPU socket on the motherboard.

2. Align the large hole on the heatsink against the large notch on the CPU socket, the small hole on the heatsink against the small notch on the socket. Carefully insert the PHM into the socket, making sure that the large and small notches fit through the corresponding mounting holes on the socket. The PHM will only fit one way. If it does not fit correctly, remove it and try again.

3. Using a T30-size star driver bit, tighten four screws into the mounting holes on the socket to securely install the PHM into the motherboard, starting with the mounting hole marked #1 (in the sequence of 1, 2, 3, and 4).

Note: Do not use excessive force when tightening the screws to avoid damaging the CPU and the socket.

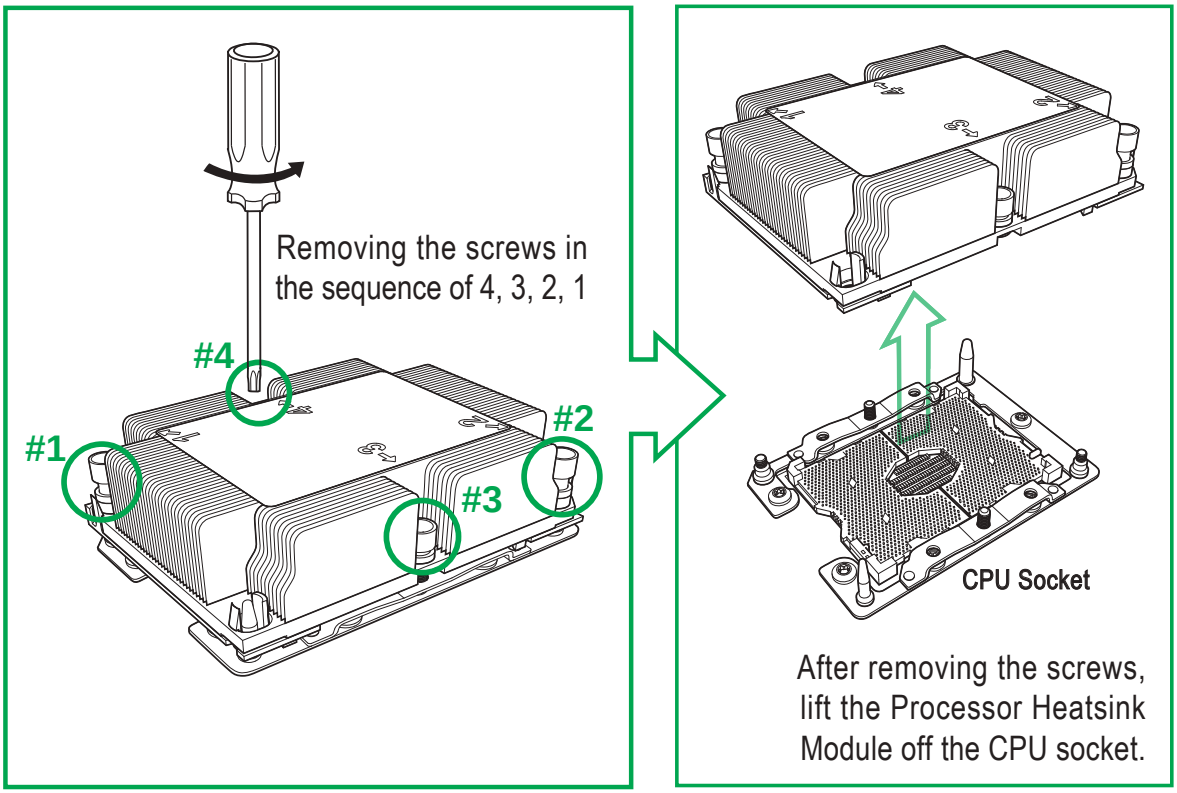


Removing the Processor Heatsink Module (PHM)

Before starting to remove the processor heatsink module (PHM), unplug power cord from the power outlet.

1. Using a T30-size star driver, turn the screws on the PHM counterclockwise to loosen it from the socket, starting with screw marked #4 (in the sequence of 4, 3, 2, 1).
2. After all four screws are removed, wiggle the PHM gently and pull up to remove it from the socket.

Note: To properly remove the processor heatsink module, be sure to loosen and remove the screws on the PHM in the sequence of 4, 3, 2, 1 as shown below.



2.4 Memory Support and Installation

Note: Check the Supermicro website for recommended memory modules. Exercise extreme care when installing or removing DIMM modules to prevent any damage.

Memory Support

The X11DPL-i supports up to up to 2TB of 3DS LRDIMM/LRDIMM/RDIMM/NV-DIMM DDR4 ECC 2933*/2666/2400/2133 MHz memory in 8 memory slots. Populating these DIMM modules with a pair of memory modules of the same type and size will result in interleaved memory, which will improve memory performance.

Note: 2933 MHz memory is supported by 2nd Gen Intel Xeon Scalable-SP (82xx/62xx series) processors only.

General Memory Population Requirements

1. Be sure to use the memory modules of the same type and speed on the motherboard. Mixing of memory modules of different types and speeds is not allowed.
2. Using unbalanced memory topology such as populating two DIMMs in one channel while populating one DIMM in another channel on the same motherboard will result in reduced memory performance.
3. Populating memory slots with a pair of DIMM modules of the same type and size will result in interleaved memory, which will improve memory performance.

DDR4 Memory Support for Intel Xeon Scalable-SP Processors

DDR4 Memory Support				
Type	Ranks Per DIMM & Data Width	DIMM Capacity (GB)		Speed (MT/s); Voltage (V); Slots Per Channel (SPC) and DIMMs Per Channel (DPC)
				1 Slot Per Channel
		DRAM Density		1DPC (1-DIMM Per Channel)
		4Gb*	8Gb	1.2 V
RDIMM	SRx4	4GB	8GB	2666
RDIMM	SRx8	8GB	16GB	2666
RDIMM	DRx8	8GB	16GB	2666
RDIMM	DRx4	16GB	32GB	2666
RDIMM 3Ds	QRX4	N/A	2H-64GB	2666
RDIMM 3Ds	8RX4	N/A	4H-128GB	2666
LRDIMM	QRx4	32GB	64GB	2666
LRDIMM 3Ds	QRX4	N/A	2H-64GB	2666
LRDIMM 3Ds	8Rx4	N/A	4H-128GB	2666

DDR4 Memory Support for 2nd Gen Intel Xeon Scalable-SP Processors

DDR4 Memory Support					
Type	Ranks Per DIMM & Data Width	DIMM Capacity (GB)			Speed (MT/s); Voltage (V); Slots Per Channel (SPC) and DIMMs Per Channel (DPC)
					1 Slot Per Channel
		DRAM Density			1DPC (1-DIMM Per Channel)
		4Gb*	8Gb	16Gb	1.2 V
RDIMM	SRx4	4GB	8GB	16GB	2933
RDIMM	SRx8	8GB	16GB	32GB	2933
RDIMM	DRx8	8GB	16GB	32GB	2933
RDIMM	DRx4	16GB	32GB	64GB	2933
RDIMM 3Ds	QRX4	N/A	2H-64GB	2H-128GB	2933
RDIMM 3Ds	8RX4	N/A	4H-128GB	4H-256GB	2933
LRDIMM	QRx4	32GB	64GB	128GB	2933
LRDIMM 3Ds	QRX4	N/A	2H-64GB	2H-128GB	2933
LRDIMM 3Ds	8Rx4	N/A	4H-128GB	4H-256GB	2933

Note: 2933 MHz memory is supported by 2nd Gen Intel Xeon Scalable-SP (82xx/62xx series) processors only.

DIMM Population Guidelines for Optimal Performance

For optimal memory performance, follow the instructions listed in the tables below when populating memory modules.

Key Parameters for DIMM Configuration

Key Parameters for DIMM Configurations	
Parameters	**Possible Values**
Number of Channels	1, 2, 3, 4, 5, or 6
Number of DIMMs per Channel	1DPC (1 DIMM Per Channel) or 2DPC (2 DIMMs Per Channel)
DIMM Type	RDIMM (w/ECC), 3DS RDIMM, LRDIMM, 3DS LRDIMM
DIMM Construction	• non-3DS RDIMM Raw Cards: A/B (2RX4), C (1RX4), D (1RX8), E (2RX8) • 3DS RDIMM Raw Cards: A/B (4RX4) • non-3DS LRDIMM Raw Cards: D/E (4RX4) • 3DS LRDIMM Raw Cards: A/B (8RX4)

DIMM Mixing Guidelines

General Population Requirements
DIMM Mixing Rules
• All DIMMs must be all DDR4 DIMMs. • x4 and x8 DIMMs can be mixed in the same channel. • Mixing of LRDIMMs and RDIMMs is not allowed in the same channel, across different channels, and across different sockets. • Mixing of non-3DS and 3DS LRDIMM is not allowed in the same channel, across different channels, and across different sockets.

Mixing of DIMM Types within a Channel			
DIMM Types	**RDIMM**	**LRDIMM**	**3DS LRDIMM**
RDIMM	Allowed	Not Allowed	Not Allowed
LRDIMM	Not Allowed	Allowed	Not Allowed
3DS LRDIMM	Not Allowed	Not Allowed	Allowed

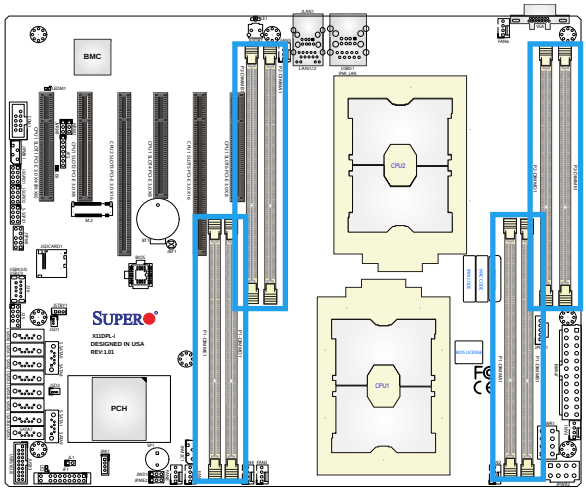
DIMM Population Table

Note: Unbalanced memory configuration decreases memory performance and is not recommended for Supermicro motherboards.

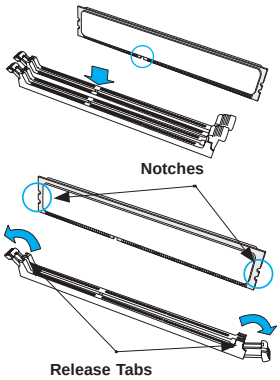
Memory Population Table for the Motherboard Using Intel Xeon Scalable-SP and 2nd Gen Intel Xeon Scalable-SP Processors

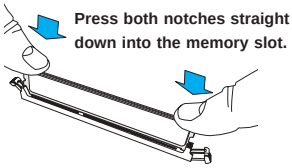
Memory Population Table for the X11DP Motherboard w/8 DIMM Slots Onboard	
When 1 CPU is used:	**Memory Population Sequence**
1 CPU & 1 DIMM	CPU1: P1-DIMMA1
1 CPU & 2 DIMMs	CPU1: P1-DIMMA1/P1-DIMMD1
1 CPU & 3 DIMMs (Unbalanced: not recommended)	CPU1: P1-DIMMB1/P1-DIMMA1/P1-DIMMD1
1 CPU & 4 DIMMs	CPU1: P1-DIMMB1/P1-DIMMA1/P1-DIMMD1/P1-DIMME1
When 2 CPUs are used:	Memory Population Sequence
2 CPUs & 2 DIMMs	CPU1: P1-DIMMA1 CPU2: P2-DIMMA1
2 CPUs & 4 DIMMs	CPU1: P1-DIMMA1/P1-DIMMD1 CPU2: P2-DIMMA1/P2-DIMMD1
2 CPUs & 6 DIMMs	CPU1: P1-DIMMB1/P1-DIMMA1/P1-DIMMD1/P1-DIMME1 CPU2: P2-DIMMA1/P2-DIMMD1
2 CPUs & 8 DIMMs	CPU1: P1-DIMMB1/P1-DIMMA1/P1-DIMMD1/P1-DIMME1 CPU2: P2-DIMMB1/P2-DIMMA1/P2-DIMMD1/P2-DIMME1

DIMM Installation



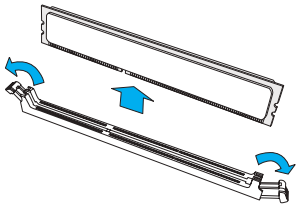
1. Follow the instructions given in the memory population guidelines listed in the previous sections to install memory modules on your motherboard. For the system to work properly, please use memory modules of the same type and speed on the motherboard.
2. Push the release tabs outwards on both ends of the DIMM slot to unlock it.
3. Align the key of the DIMM module with the receptive point on the memory slot.
4. Align the notches on both ends of the module against the receptive points on the ends of the slot.
5. Use two thumbs together to press the DIMM module straight down into the slot until the module snaps into place.
6. Press the release tabs to the lock positions to secure the DIMM module into the slot.





DIMM Module Removal

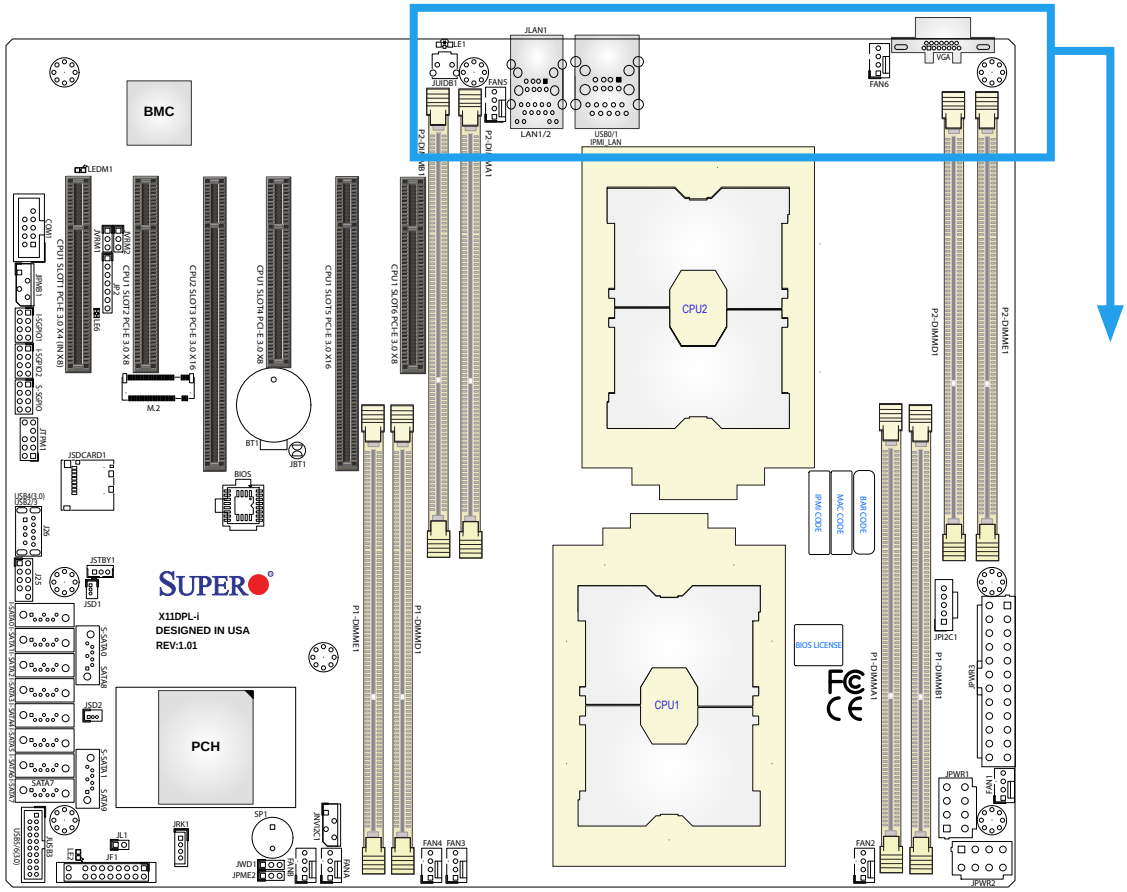
Press the release tabs on both ends of the DIMM socket to release the DIMM module from the socket as shown in the drawing on the right.



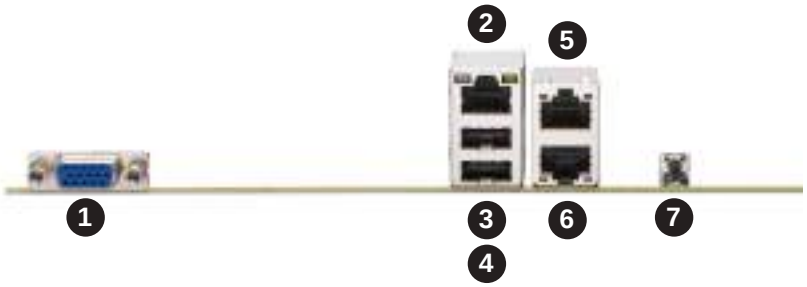
Warnings! 1. Please do not use excessive force when pressing the release tabs on the ends of the DIMM socket to avoid causing any damage to the DIMM module or the DIMM socket. **2.** Please handle DIMM modules with care. Carefully follow all the instructions given on Page 1 of this chapter to avoid ESD-related damages done to your components or system. **3.** All graphics including the layout drawing above are for reference only. Your system components may look or may not look the same as graphics shown in this user guide.

2.5 Rear I/O Ports

See the figure below for the locations and descriptions of the various I/O ports on the rear of the motherboard.



Back panel I/O Port Locations and Definitions



Back Panel I/O Ports			
No.	**Description**	**No.**	**Description**
1.	VGA port	5.	LAN2
2.	Dedicated IPMI LAN	6.	LAN1
3.	USB1 (2.0)	7.	Unit Identifier Switch
4.	USB0 (2.0)		

VGA Port

The onboard VGA port is located next to IPMI LAN port on the I/O back panel. Use this connection for VGA display.

Unit Identifier Switch/UID LED Indicator

A Unit Identifier (UID) switch (UID) and a UID LED Indicator (LE1) are located on the I/O back panel. When you press the UID switcWh, the UID LED indicator will be turned on. Press the UID switch again to turn off the LED. The UID Indicator provides easy identification of a system unit that may be in need of service.

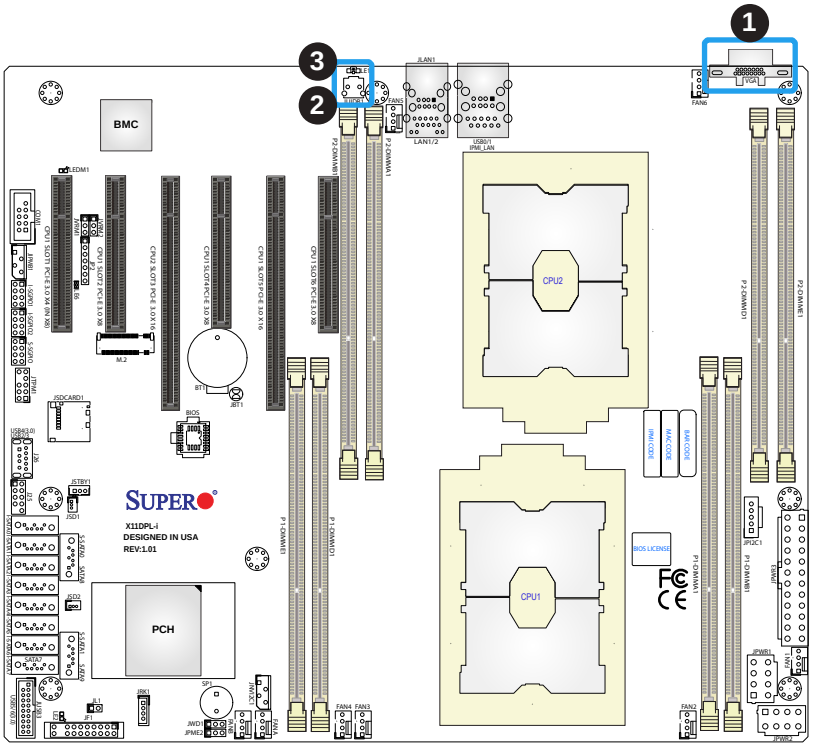
Note: UID can also be triggered via IPMI on the motherboard. For more information on IPMI, please refer to the IPMI User's Guide posted on our website at http://www.supermicro.com.

UID LED Pin Definitions

Color	Status
Blue: On	Unit Identified

UID Switch Pin Definitions

Pin#	Definition
1	Button In
2	Ground
3	Ground
4	Ground



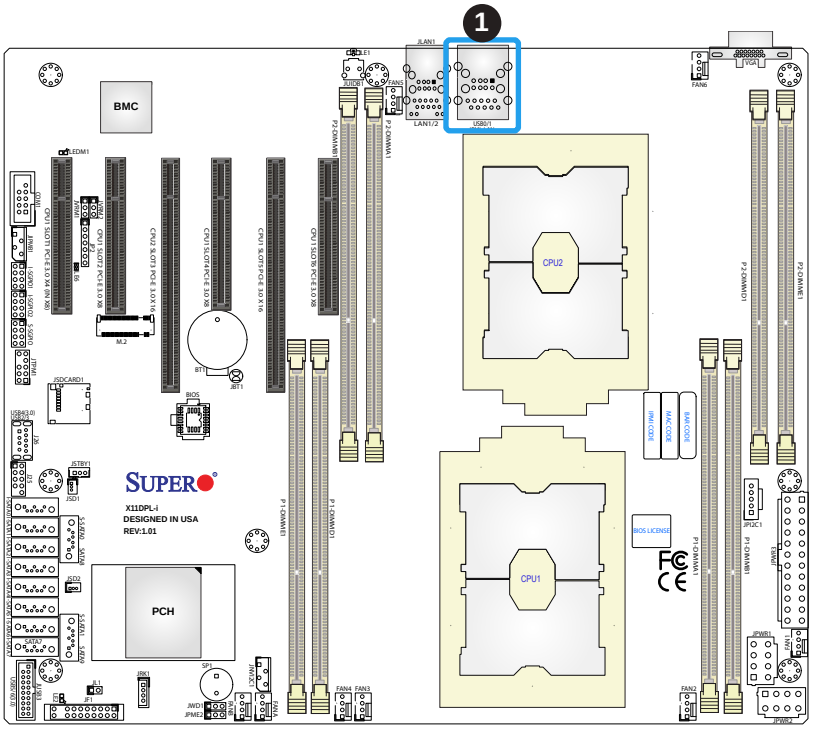
1. VGA Port
2. UID Switch
3. UID LED

Universal Serial Bus (USB) Ports

There are two USB 2.0 ports (USB0/USB1) on the I/O back panel on the motherboard to provide USB connection.

Back Panel USB 0/1 (2.0) Pin Definitions

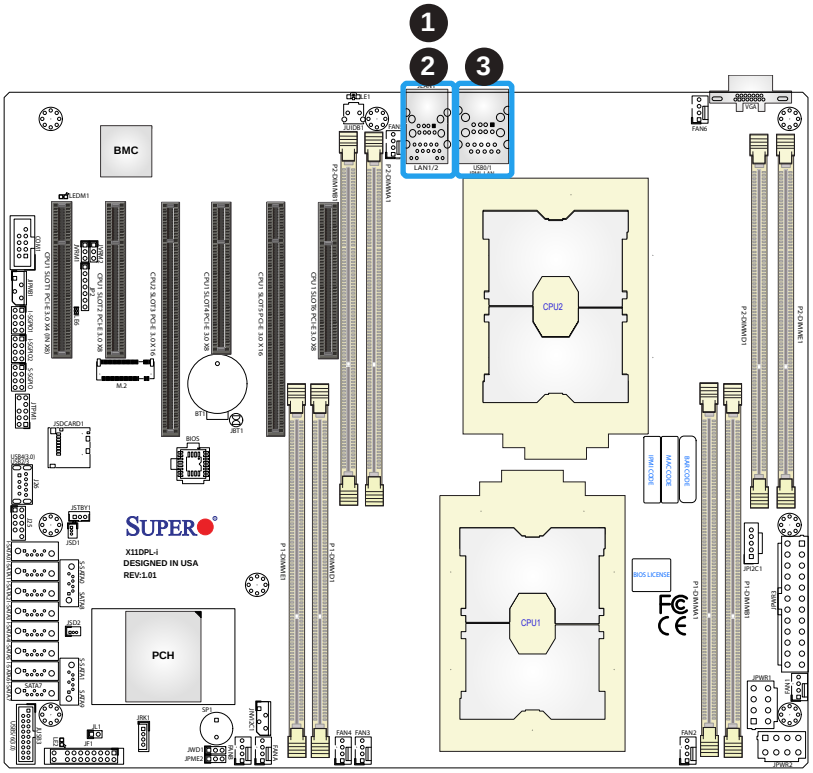
Pin#	Definition	Pin#	Definition
1	+5V	5	+5V
2	USB_N	6	USB_N
3	USB_P	7	USB_P
4	Ground	8	Ground



1. USB0/USB1

Ethernet Ports

Two LAN ports (LAN1/LAN2) and a dedicated IPMI LAN are located on the I/O back panel. IPMI_LAN is supported by the onboard AST 2500 BMC and accepts an RJ45 type cable. Refer to the LED Indicator Section for LAN LED information.



1. LAN2
2. LAN1
3. IPMI LAN

2.6 Front Control Panel

JF1 contains header pins for various buttons and indicators that are normally located on a control panel at the front of the chassis. These connectors are designed specifically for use with Supermicro chassis. See the figure below for the descriptions of the front control panel buttons and LED indicators.

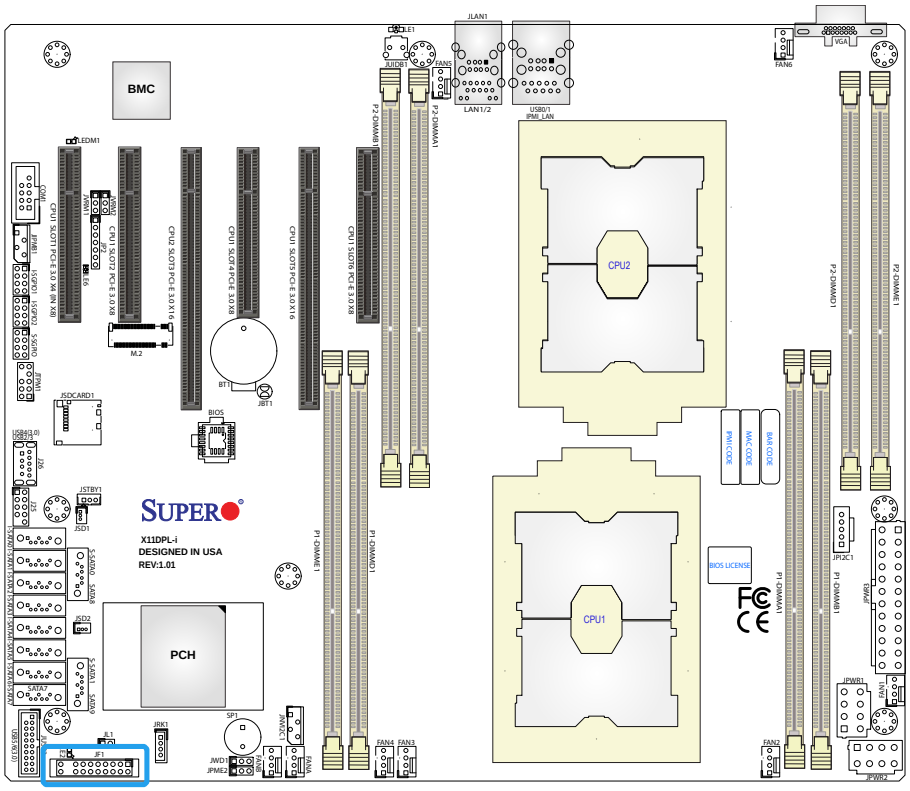


Figure 2-3. JF1 Header Pins

	1	2	
Power Button	○	○	Ground
Reset Button	○	○	Ground
3.3V	○	○	Power Fail LED
UID LED	○	○	OH/Fan Fail LED
3.3V Stby	○	○	NIC2 Active LED
3.3V Stby	○	○	NIC1 Active LED
3.3V Stby	○	○	HDD LED
3.3V	○	○	PWR LED
X	○	○	X
NMI	○	○	Ground
	19	20	

Power Button

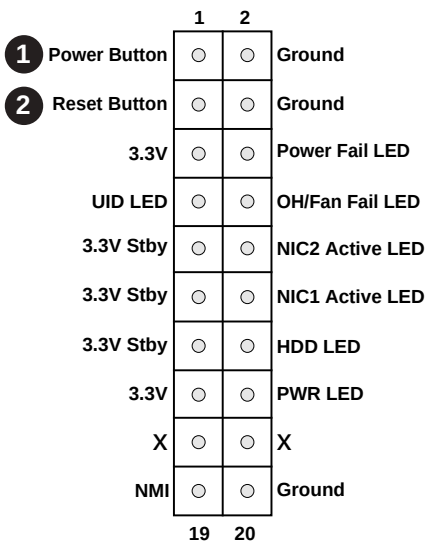
The Power Button connection is located on pins 1 and 2 of JF1. Momentarily contacting both pins will power on/off the system. This button can also be configured to function as a suspend button (with a setting in the BIOS - see Chapter 4). To turn off the power when the system is in suspend mode, press the button for 4 seconds or longer. Refer to the table below for pin definitions.

Power Button Pin Definitions (JF1)	
Pins	**Definition**
1	Signal
2	Ground

Reset Button

The Reset Button connection is located on pins 3 and 4 of JF1. Attach it to a hardware reset switch on the computer case to reset the system. Refer to the table below for pin definitions.

Reset Button Pin Definitions (JF1)	
Pins	**Definition**
3	Reset
4	Ground



1. PWR Button
2. Reset Button

Power Fail LED

The Power Fail LED connection is located on pins 15 and 16 of JF1. Refer to the table below for pin definitions.

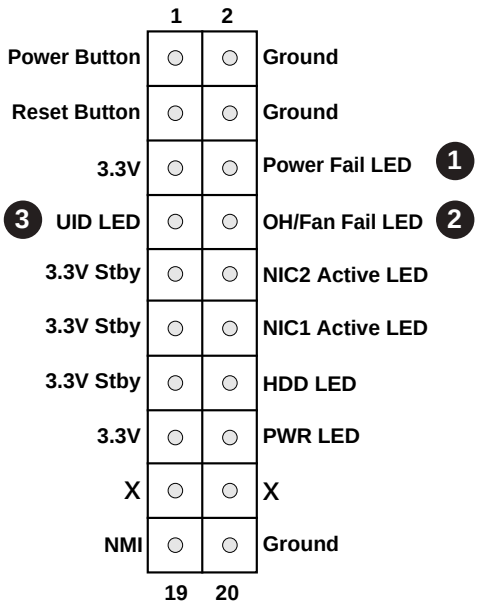
Power LED Pin Definitions (JF1)	
Pins	**Definition**
5	+3.3V
6	Power Fail LED

Fan Fail and UID LED

Connect an LED cable to pins 7 and 8 of the Front Control Panel to use the Overheat/Fan Fail LED connections. The LED on pin 8 provides warnings of overheat or fan failure. Refer to the tables below for pin definitions.

OH/Fan Fail Indicator Status	
State	**Definition**
Off	Normal
On	Overheat
Flashing	Fan Fail

OH/Fan Fail LED Pin Definitions (JF1)	
Pin#	**Definition**
7	UID LED
8	OH/Fan Fail LED



1. Power Fail LED
2. OH/Fan Fail LED
3. UID LED

NIC1/NIC2 (LAN1/LAN2)

The NIC (Network Interface Controller) LED connection for LAN port 1 is located on pins 11 and 12 of JF1, and the LED connection for LAN Port 2 is on pins 9 and 10. Attach NIC LED cables to NIC1 and NIC2 LED indicators to display network activities. Refer to the table below for pin definitions.

LAN1/LAN2 LED Pin Definitions (JF1)

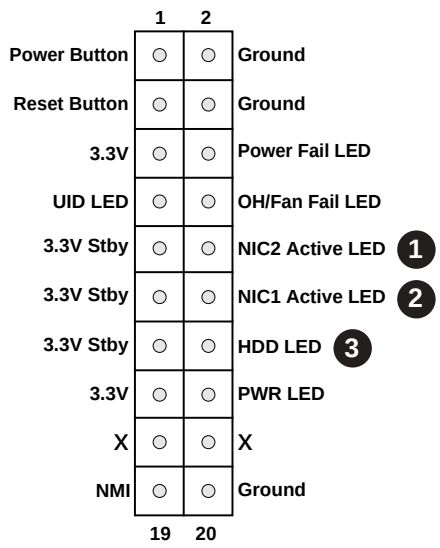
Pins	Definition
9/11	+3.3V Stby
10/12	NIC Activity LED

HDD LED

The HDD LED connection is located on pins 13 and 14 of JF1. Attach a cable to pin 14 to show hard drive activity status. Refer to the table below for pin definitions.

HDD LED Pin Definitions (JF1)

Pins	Definition
13	3.3V Stdby
14	HDD Active



1. NIC2 LED
2. NIC1 LED
3. HDD LED

Power LED

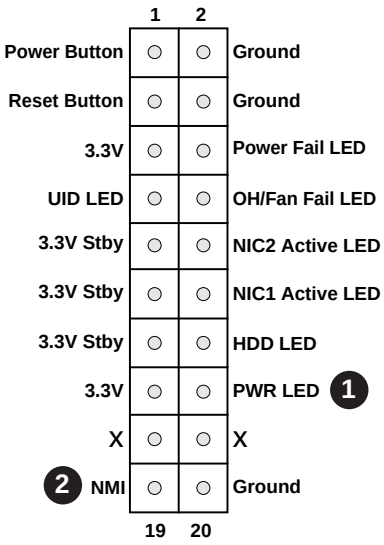
The Power LED connection is located on pins 15 and 16 of JF1. Refer to the table below for pin definitions.

Power LED Pin Definitions (JF1)	
Pins	**Definition**
15	+3.3V
16	PWR LED

NMI Button

The non-maskable interrupt (NMI) button header is located on pins 19 and 20 of JF1. Refer to the table below for pin definitions.

NMI Button Pin Definitions (JF1)	
Pins	**Definition**
19	Control
20	Ground



1. Power LED
2. NMI

2.7 Connectors

Power Connector

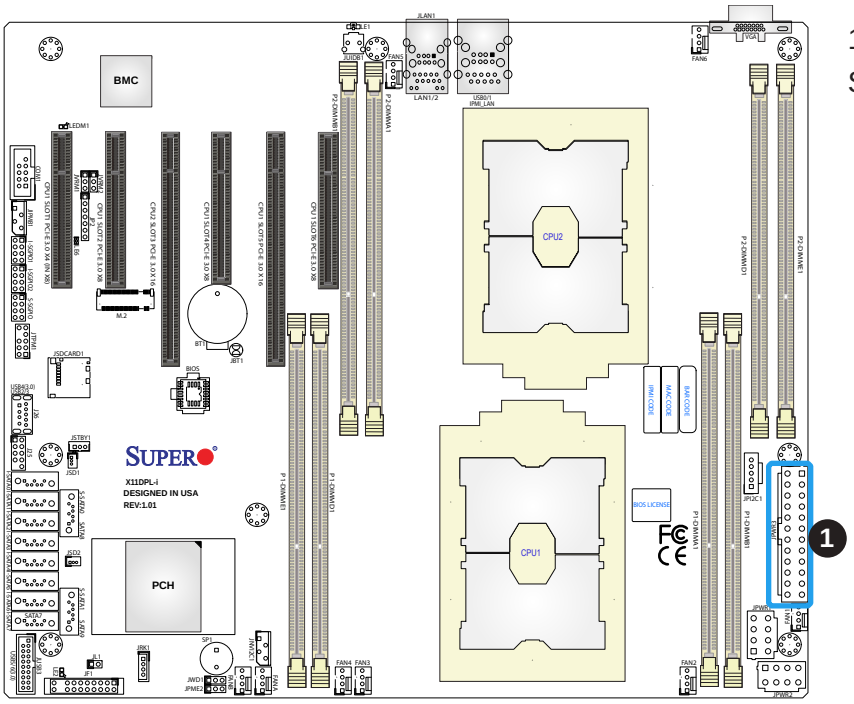
ATX and CPU Power Connectors

JPWR3 is the 24-pin ATX main power supply connector. This primary power supply connector meets the ATX SSI EPS 24-pin specification. You must also connect the 8-pin (JPWR1/ JPWR2) CPU power connectors to your power supply.

ATX Power 24-pin Connector Pin Definitions

Pin#	Definition	Pin#	Definition
13	+3.3V	1	+3.3V
14	NC	2	+3.3V
15	Ground	3	Ground
16	PS_ON	4	+5V
17	Ground	5	Ground
18	Ground	6	+5V
19	Ground	7	Ground
20	Res (NC)	8	PWR_OK
21	+5V	9	5VSB
22	+5V	10	+12V
23	+5V	11	+12V
24	Ground	12	+3.3V

Required Connection

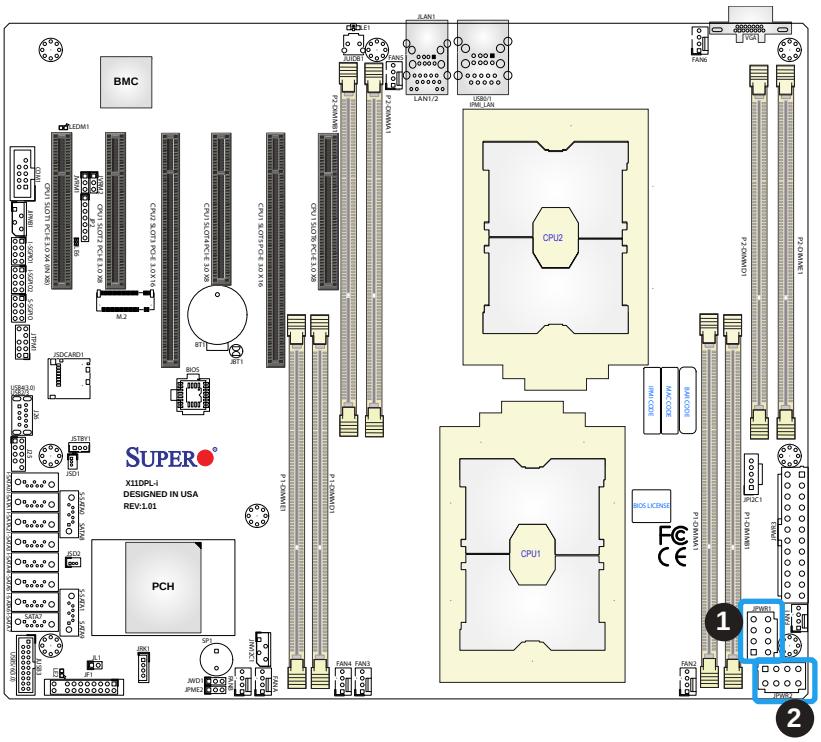


1. JPWR3 24-pin Power Supply

12V 8-pin CPU Power Connectors

JPWR1 and JPWR2 are the 8-pin 12V DC power input for the CPU or alternative single power source for a special enclosure when the 24-pin ATX power is not in use. Refer to the table below for pin definitions.

12V 8-pin Power Pin Definitions	
Pin#	**Definition**
1 - 4	Ground
5 - 8	+12V



1. JPWR1
2. JPWR2

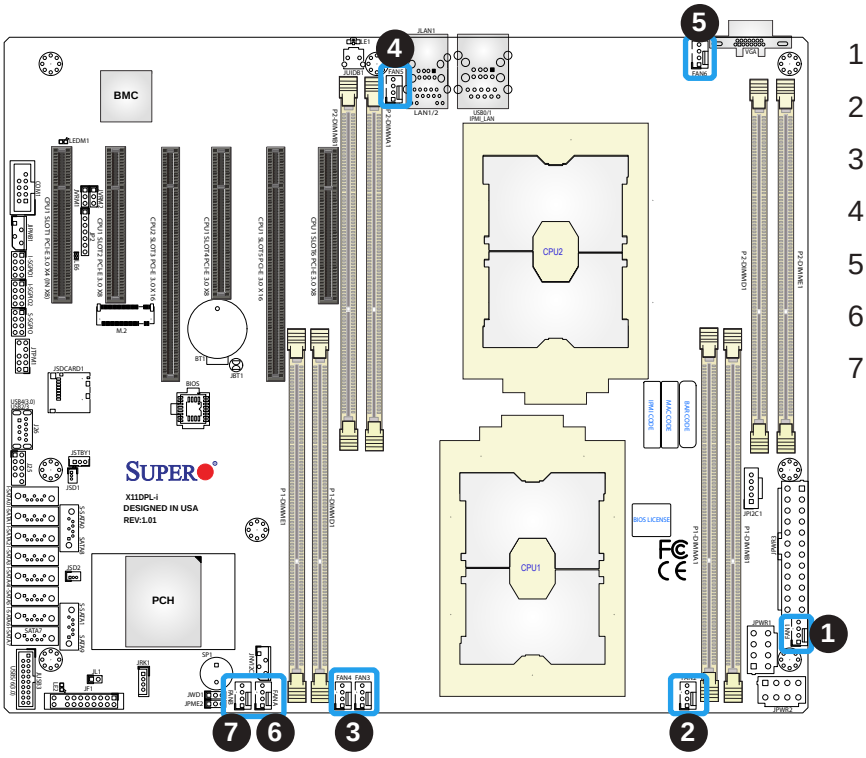
Headers

Onboard Fan Headers

This motherboard has eight fan headers (FAN1~FAN6, FANA, FANB). All these 4-pin fan headers are backward-compatible with traditional 3-pin fans. However, onboard fan speed control is available only when all 4-pin fans are used on the motherboard. Fan speed control is supported by Thermal Management via IPMI 2.0 interface. See the table below for pin definitions.

Fan Header Pin Definitions

Pin#	Definition
1	Ground (Black)
2	+12V (Red)
3	Tachometer
4	PWM Control



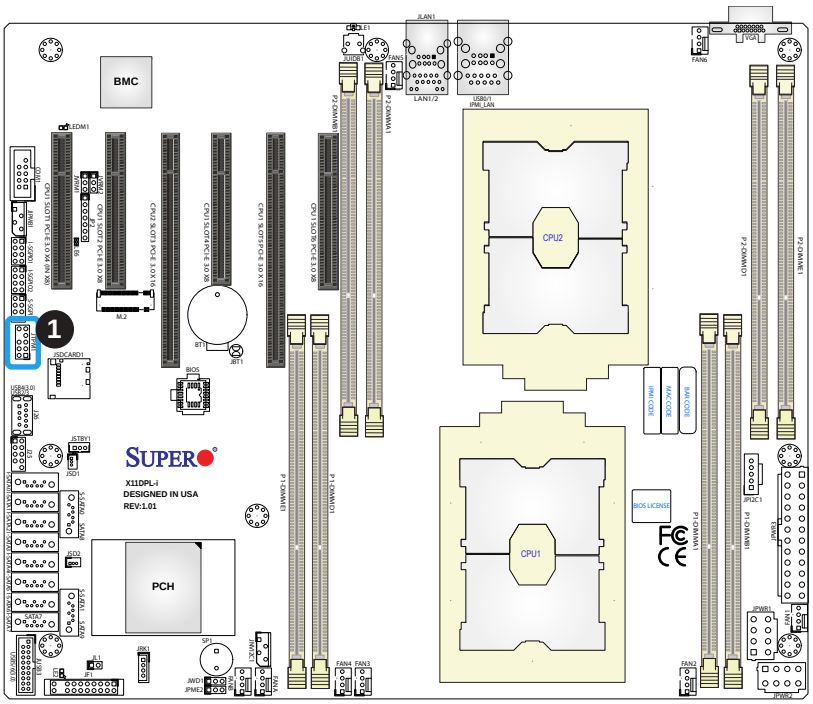
1. FAN1
2. FAN2
3. FAN3/FAN4
4. FAN5
5. FAN6
6. FANA
7. FANB

TPM Header

The JTPM1 header is used to connect a Trusted Platform Module (TPM)/Port 80, which is available from Supermicro (optionally). A TPM/Port 80 connector is a security device that supports encryption and authentication in hard drives. It allows the motherboard to deny access if the TPM associated with the hard drive is not installed in the system. See the table below for pin definitions.

Trusted Platform Module/Port 80 Header Pin Definitions

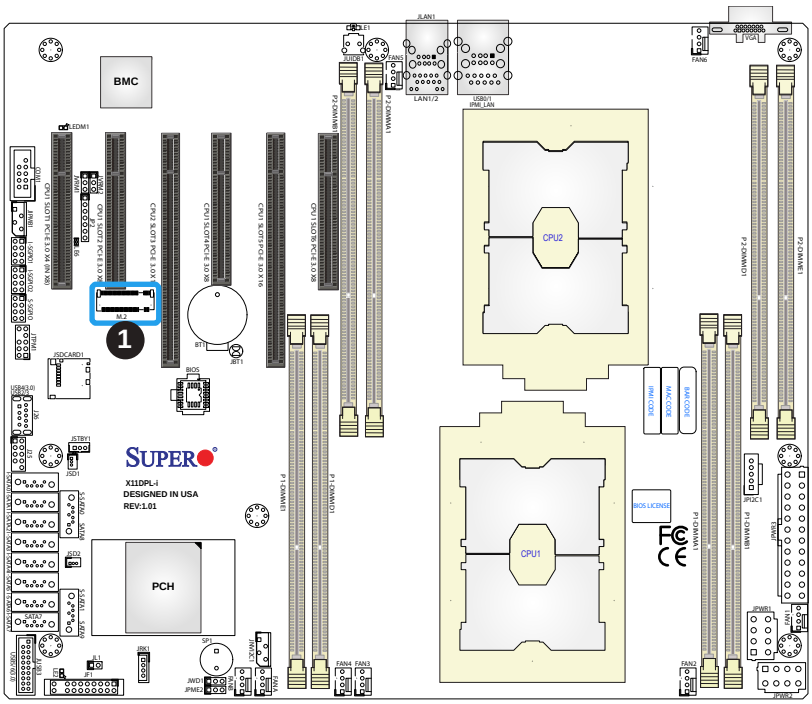
Pin#	Definition	Pin#	Definition
1	+3.3V	2	SPI_CS#
3	RESET#	4	SPI_MISO
5	SPI_CLK	6	GND
7	SPI_MOSI	8	
9	+3.3V Stdby	10	SPI_IRQ#



1. TPM/Port 80 Header

PCI-E M.2 Slot

The X11DPL-i motherboard has one PCI-E M.2 slot that is supported by CPU1 (M.2-CPU1). M.2 was formerly Next Generation Form Factor (NGFF) and are used to replace mini PCI-E. M.2 supports a variety of card sizes with increased functionality and spatial efficiency. The M.2 socket on the motherboard supports SATA/PCI-E 3.0 X4 (32 Gb/s) SSD cards in the M-key 2260, 2280 form factors.

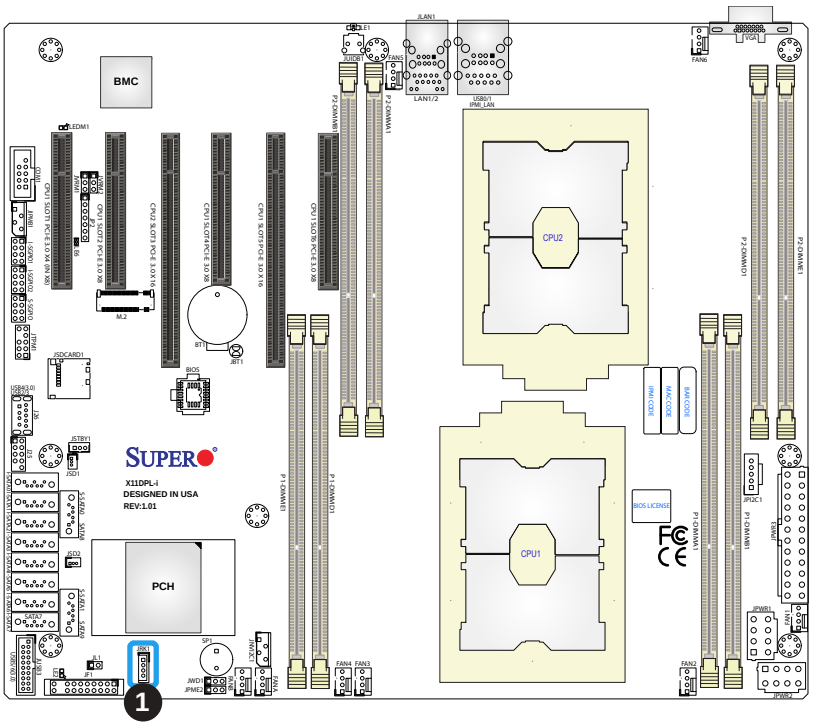


1. PCI-E M.2 Slot

RAID Key Header

A RAID Key header is located at JRK1 on the motherboard. The RAID key is used RAID settings for NVMe connections.

Intel RAID Key Pin Definitions	
Pins	**Definition**
1	GND
2	3.3V Stdby
3	GND
4	PCH RAID KEY



1. RAID Key Header

4-pin BMC External I2C Header

A System Management Bus header for IPMI 2.0 is located at JIPMB1. Connect the appropriate cable here to use the IPMB I2C connection on your system. Refer to the table below for pin definitions.

External I²C Header Pin Definitions

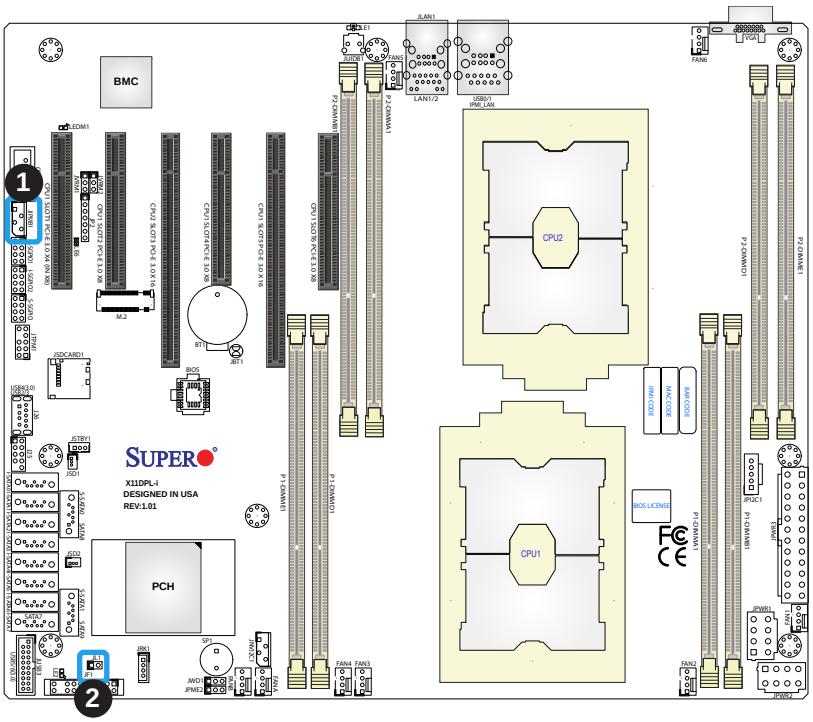
Pin#	Definition
1	Data
2	Ground
3	Clock
4	No Connection

Chassis Intrusion

A Chassis Intrusion header is located at JL1 on the motherboard. Attach the appropriate cable from the chassis to inform you of a chassis intrusion when the chassis is opened. Refer to the table below for pin definitions.

Chassis Intrusion Pin Definitions

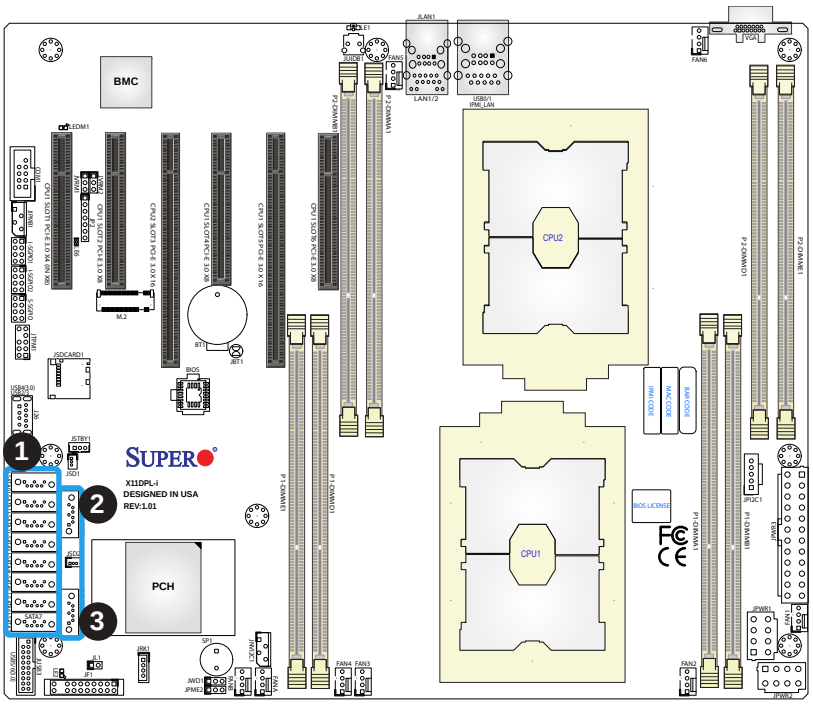
Pin#	Definition
1	Intrusion Input
2	Ground



1. BMC External I²C Header
2. Chassis Intrusion

I-SATA 3.0 and S-SATA 3.0 Ports

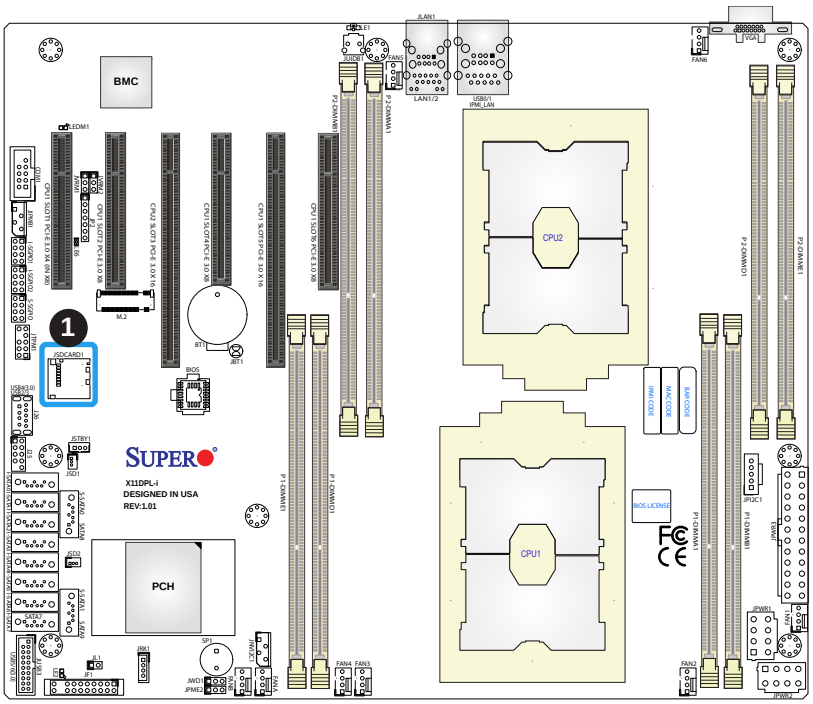
The X11DPL-i has eight I-SATA 3.0 ports (I-SATA0~7) and two S-SATA 3.0 ports (S-SATA0/1) on the motherboard. These SATA ports are supported by the Intel C621 chipset. All these SATA ports provide serial-link signal connections, which are faster than the connections of Parallel ATA.



1. I-SATA0-7
2. S-SATA0
3. S-SATA1

Micro SD Card

There is one Micro SD memory card slot located at JSDCARD1 on the motherboard. The Micro SD slot allows users to insert the Micro SD memory card to store information. Refer to the motherboard layout below for its location.



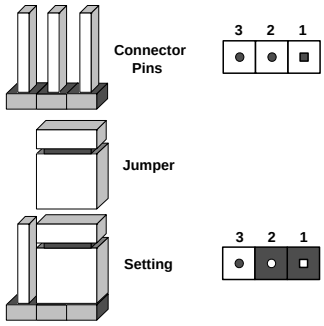
1. JSDCARD1

2.8 Jumper Settings

How Jumpers Work

To modify the operation of the motherboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. See the diagram at right for an example of jumping pins 1 and 2. Refer to the motherboard layout page for jumper locations.

Note: On two-pin jumpers, "Closed" means the jumper is on and "Open" means the jumper is off the pins.



CMOS Clear

JBT1 is used to clear CMOS, which will also clear any passwords. Instead of pins, this jumper consists of contact pads to prevent accidentally clearing the contents of CMOS.

To Clear CMOS

1. First power down the system and unplug the power cord(s).
2. Remove the cover of the chassis to access the motherboard.
3. Remove the onboard battery from the motherboard.
4. Short the CMOS pads with a metal object such as a small screwdriver for at least four seconds.
5. Remove the screwdriver (or shorting device).
6. Replace the cover, reconnect the power cord(s), and power on the system.

Note: Clearing CMOS will also clear all passwords.

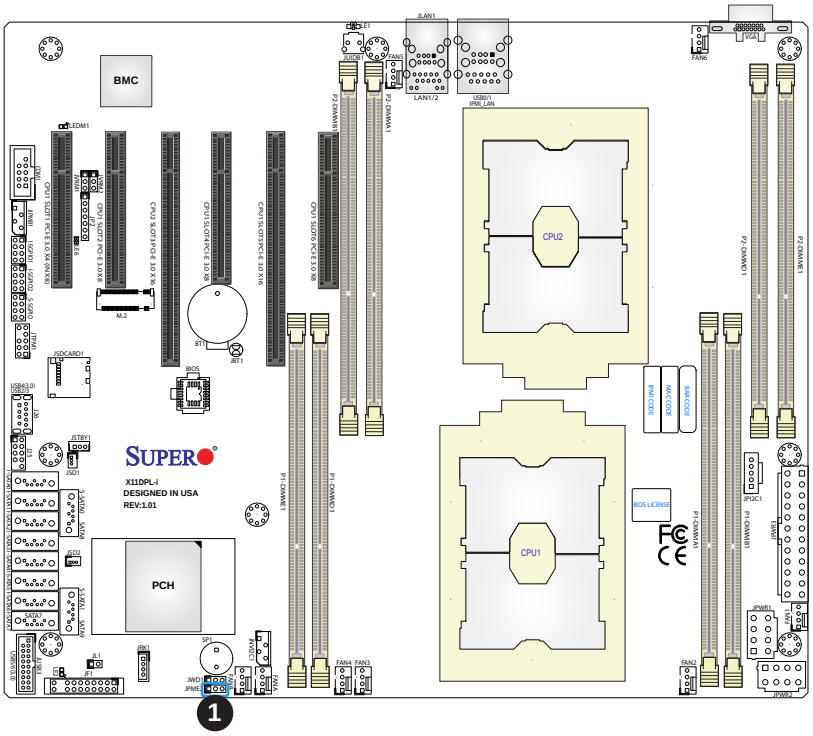


Do not use the PW_ON connector to clear CMOS.

Manufacturing Mode

Close JPME2 to bypass SPI flash security and force the system to use the Manufacturing Mode, which will allow you to flash the system firmware from a host server to modify system settings. See the table below for jumper settings.

Manufacturing Mode Select Jumper Settings	
Jumper Setting	**Definition**
Pins 1-2	Normal (Default)
Pins 2-3	Manufacturing Mode

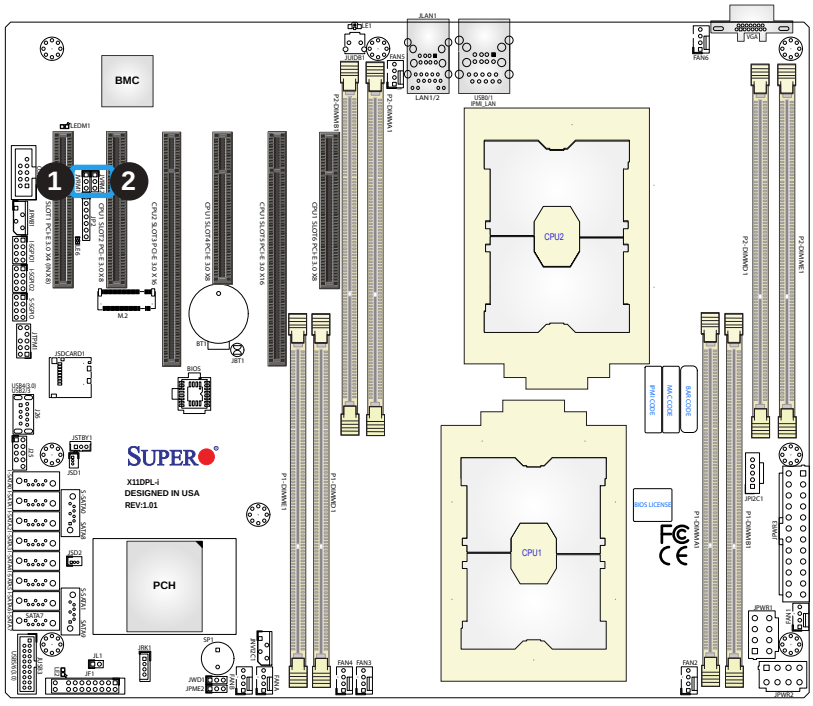


1. Manufacturing Mode

I²C Bus for VRM

Jumpers JVRM1 and JVRM2 allow the BMC or the PCH to access CPU and memory VRM controllers. See the table below for jumper settings.

VRM Jumper Settings		
Jumper	**Jumper Setting**	**Definition**
JVRM1	Pins 1-2	SMB Clock to BMC/PCH
JVRM2	Pins 1-2	SMB Data to BMC/PCH



1. JVRM1
2. JVRM2

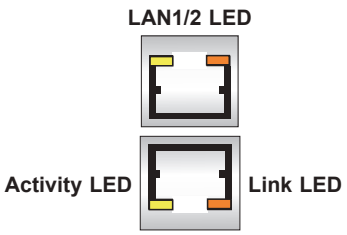
2.9 LED Indicators

LAN1/2 LEDs

LAN1 and LAN2 have two LED indicators respectively. The yellow LED on the left of the LAN port indicates activity, while the amber LED on the right indicates the speed of the connection. See the table below for more information.

GLAN Port 1/2 Link Indicator LED Settings	
Color	Definition
Off	No Connection
Amber	1Gbps

GLAN Port 1/2 Activity Indicator LED Settings		
Color	Status	Definition
Yellow	Flashing	Active



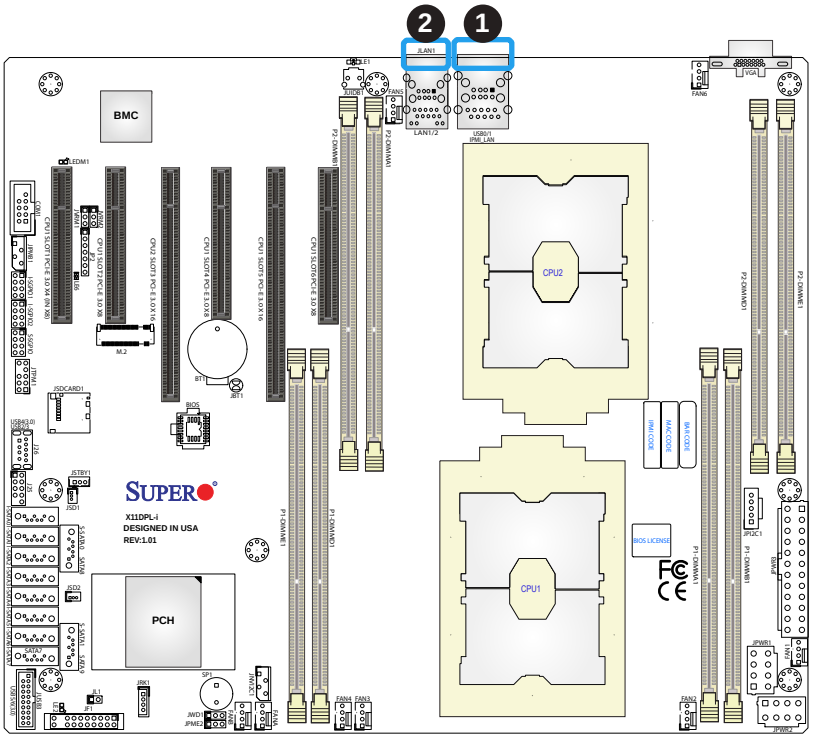
Rear View (when facing the rear side of the chasis)

IPMI LAN LEDs

A dedicated IPMI LAN, located on the back panel, has two LED indicators. The amber LED on the right of the IPMI LAN port indicates activity, while the green LED on the left indicates the speed of the connection. See the table below for more information.

IPMI LAN LEDs		
	Color/State	Definition
Link (left)	Green: Solid Amber: Solid	100 Mbps 1Gbps
Activity (Right)	Amber: Blinking	Active



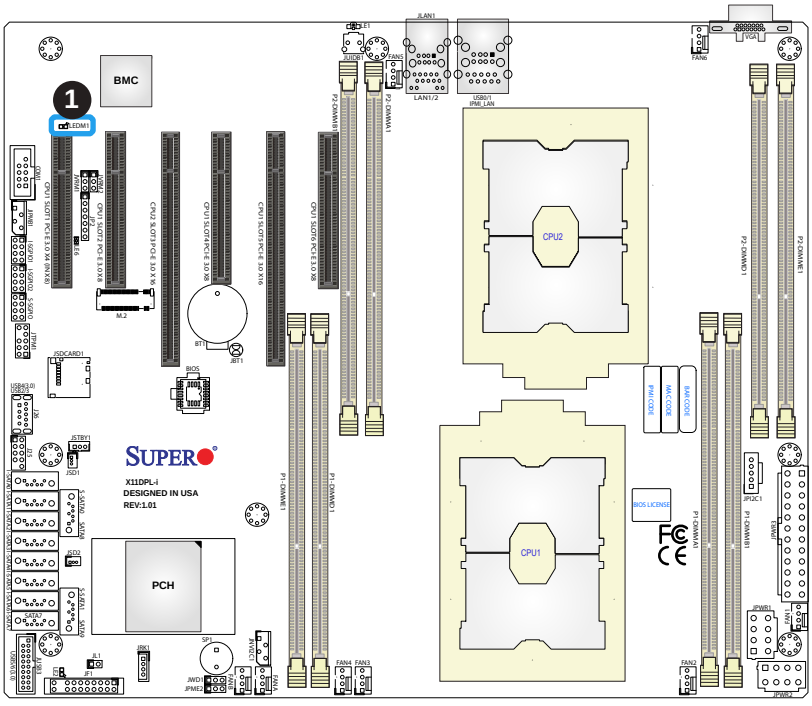


1. IPMI LAN LEDs
2. LAN1/LAN2 LEDs

BMC Heartbeat LED

LEDM1 is the BMC heartbeat LED. When the LED is blinking green, BMC is functioning normally. See the table below for the LED status.

BMC Heartbeat LED Indicator	
LED Color	**Definition**
Green: Blinking	BMC Normal

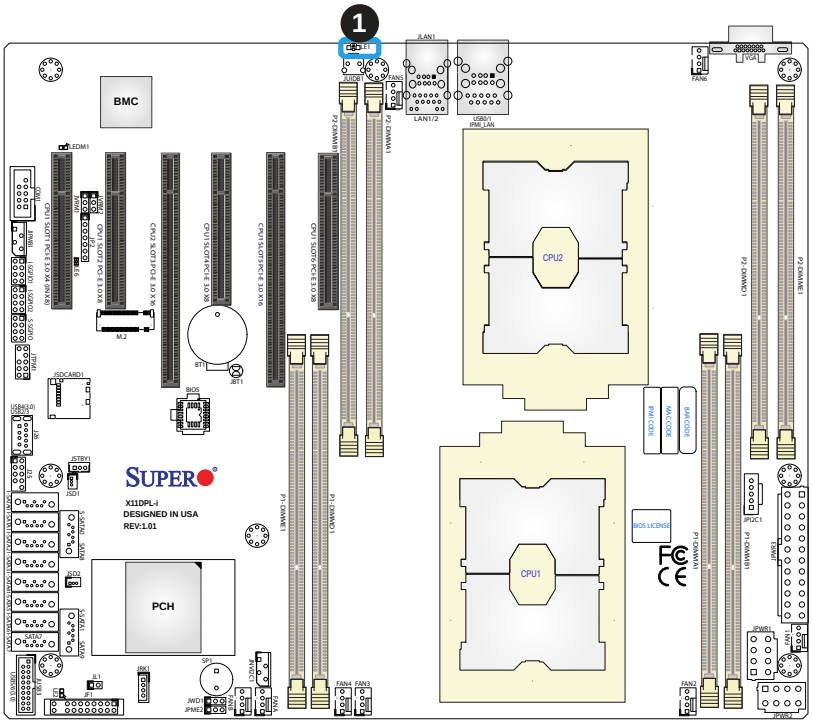


1. BMC Heartbeat LED

Unit ID LED

A rear UID LED indicator at UID_LED1 is located near the UID switch on the back panel. This UID indicator provides easy identification of a system unit that may need service.

UID LED LED Indicator	
LED Color	**Definition**
Blue: On	Unit Identified

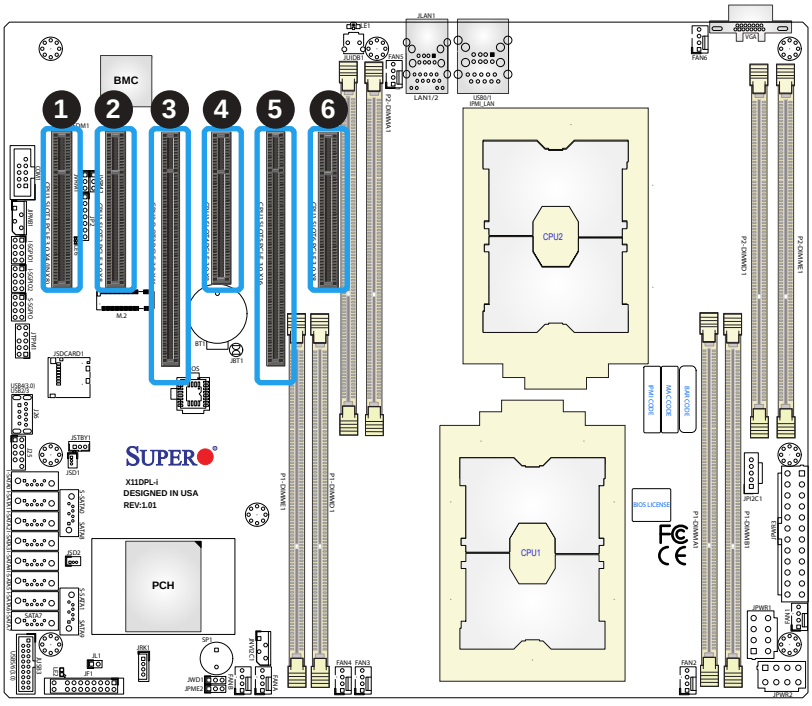


1. UID LED

2.10 PCI-E 3.0 Slots

PCI-E 3.0 Slots

There are several PCI-E slots located on the motherboard. Refer to the layout below for their locations.



1. PCI-E 3.0 x4 (in x8) (CPU1)
2. PCI-E 3.0 x8 (CPU1)
3. PCI-E 3.0 x16 (CPU2)
4. PCI-E 3.0 x8 (CPU1)
5. PCI-E 3.0 x16 (CPU1)
6. PCI-E 3.0 x8 (CPU1)

Chapter 3

Troubleshooting

3.1 Troubleshooting Procedures

Use the following procedures to troubleshoot your system. If you have followed all of the procedures below and still need assistance, refer to the 'Technical Support Procedures' and/or 'Returning Merchandise for Service' section(s) in this chapter. Always disconnect the AC power cord before adding, changing or installing any non hot-swap hardware components.

Before Power On

1. Check that the power LED on the motherboard is on.
2. Make sure that the power connector is connected to your power supply.
3. Make sure that no short circuits exist between the motherboard and chassis.
4. Disconnect all cables from the motherboard, including those for the keyboard and mouse.
5. Remove all add-on cards.
6. Install a CPU, a heatsink*, and connect the internal speaker and the power LED to the motherboard. Check all jumper settings as well. (Make sure that the heatsink is fully seated.)
7. Use the correct type of onboard CMOS battery (CR2032) as recommended by the manufacturer. To avoid possible explosion, do not install the CMOS battery upside down.

No Power

1. Make sure that no short circuits exist between the motherboard and the chassis.
2. Verify that all jumpers are set to their default positions.
3. Check that the 115V/230V switch on the power supply is properly set.
4. Turn the power switch on and off to test the system.
5. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.

No Video

1. If the power is on but you have no video, remove all the add-on cards and cables.
2. Use the speaker to determine if any beep codes exist. Refer to Appendix A for details on beep codes.

System Boot Failure

If the system does not display POST (Power-On-Self-Test) or does not respond after the power is turned on, check the following:

1. Check for any error beep from the motherboard speaker.

- If there is no error beep, try to turn on the system without DIMM modules installed. If there is still no error beep, replace the motherboard.
- If there are error beeps, clear the CMOS settings by unplugging the power cord and contacting both pads on the CMOS Clear Jumper (JBT1). Refer to chapter 2.

2. Remove all components from the motherboard, especially the DIMM modules. Make sure that system power is on and that memory error beeps are activated.
3. Turn on the system with only one DIMM module installed. If the system boots, check for bad DIMM modules or slots by following the Memory Errors Troubleshooting procedure in this Chapter.

Memory Errors

1. Make sure that the DIMM modules are properly and fully installed.
2. Confirm that you are using the correct memory. Also, it is recommended that you use the same memory type and speed for all DIMMs in the system. See Section 2.4 for memory details.
3. Check for bad DIMM modules or slots by swapping modules between slots and noting the results.
4. Check the power supply voltage 115V/230V switch.

Losing the System's Setup Configuration

1. Make sure that you are using a high quality power supply. A poor quality power supply may cause the system to lose the CMOS setup information. Refer to Section 1.6 for details on recommended power supplies.

2. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.

3. If the above steps do not fix the setup configuration problem, contact your vendor for repairs.

When the System Becomes Unstable

A. If the system becomes unstable during or after OS installation, check the following:

1. CPU/BIOS support: Make sure that your CPU is supported and that you have the latest BIOS installed in your system.

2. Memory support: Make sure that the memory modules are supported by testing the modules using memtest86 or a similar utility.

   Note: Refer to the product page on our website at http:\\www.supermicro.com for memory and CPU support and updates.

3. HDD support: Make sure that all hard disk drives (HDDs) work properly. Replace the bad HDDs with good ones.

4. System cooling: Check the system cooling to make sure that all heatsink fans and CPU/system fans, etc., work properly. Check the hardware monitoring settings in the IPMI to make sure that the CPU and system temperatures are within the normal range. Also check the front panel Overheat LED and make sure that it is not on.

5. Adequate power supply: Make sure that the power supply provides adequate power to the system. Make sure that all power connectors are connected. Please refer to our website for more information on the minimum power requirements.

6. Proper software support: Make sure that the correct drivers are used.

B. If the system becomes unstable before or during OS installation, check the following:

1. Source of installation: Make sure that the devices used for installation are working properly, including boot devices such as CD.

2. Cable connection: Check to make sure that all cables are connected and working properly.

3. Using the minimum configuration for troubleshooting: Remove all unnecessary components (starting with add-on cards first), and use the minimum configuration (but with a CPU and a memory module installed) to identify the trouble areas. Refer to the steps listed in Section A above for proper troubleshooting procedures.

4. Identifying bad components by isolating them: If necessary, remove a component in question from the chassis, and test it in isolation to make sure that it works properly. Replace a bad component with a good one.

5. Check and change one component at a time instead of changing several items at the same time. This will help isolate and identify the problem.

6. To find out if a component is good, swap this component with a new one to see if the system will work properly. If so, then the old component is bad. You can also install the component in question in another system. If the new system works, the component is good and the old system has problems.

3.2 Technical Support Procedures

Before contacting Technical Support, please take the following steps. Also, note that as a motherboard manufacturer, we do not sell directly to end-users, so it is best to first check with your distributor or reseller for troubleshooting services. They should know of any possible problem(s) with the specific system configuration that was sold to you.

1. Please review the 'Troubleshooting Procedures' and 'Frequently Asked Questions' (FAQs) sections in this chapter or see the FAQs on our website before contacting Technical Support.

2. BIOS upgrades can be downloaded from our website. **Note:** Not all BIOS can be flashed depending on the modifications to the boot block code.

3. If you still cannot resolve the problem, include the following information when contacting us for technical support:

- Motherboard model and PCB revision number
- BIOS release date/version (this can be seen on the initial display when your system first boots up)
- System configuration

An example of a Technical Support form is posted on our website.

Distributors: For immediate assistance, please have your account number ready when contacting our technical support department by e-mail.

3.3 Frequently Asked Questions

Question: What type of memory does my motherboard support?

Answer: The X11DPL-i motherboard supports up to 2TB of 3DS LRDIMM/LRDIMM/RDIMM/NV-DIMM DDR4 ECC 2933/2666/2400/2133 MHz memory in 8 memory slots. See Section 2.4 for details on installing memory.

Note: 2933 MHz memory is supported by 2nd Gen Intel Xeon Scalable-SP (82xx/62xx series) processors only.

Question: How do I update my BIOS?

Answer: It is recommended that you **do not** upgrade your BIOS if you are not experiencing any problems with your system. Updated BIOS files are located on our website at http://www.supermicro.com. Please check our BIOS warning message and the information on how to update your BIOS on our website. Select your motherboard model and download the BIOS file to your computer. Also, check the current BIOS revision to make sure that it is newer than your BIOS before downloading. You can choose from the zip file and the .exe file. If you choose the zip BIOS file, please unzip the BIOS file onto a bootable USB device. Run the batch file using the format FLASH.BAT filename.rom from your bootable USB device to flash the BIOS. Then, your system will automatically reboot.

Question: Why can't I turn off the power using the momentary power on/off switch?

Answer: The instant power off function is controlled in BIOS by the Power Button Mode setting. When the On/Off feature is enabled, the motherboard will have instant off capabilities as long as the BIOS has control of the system. When the Standby or Suspend feature is enabled or when the BIOS is not in control such as during memory count (the first screen that appears when the system is turned on), the momentary on/off switch must be held for more than four seconds to shut down the system. This feature is required to implement the ACPI features on the motherboard.

3.4 Battery Removal and Installation

Battery Removal

To remove the onboard battery, follow the steps below:

1. Power off your system and unplug your power cable.
2. Locate the onboard battery as shown below.
3. Using a tool such as a pen or a small screwdriver, push the battery lock outwards to unlock it. Once unlocked, the battery will pop out from the holder.
4. Remove the battery.

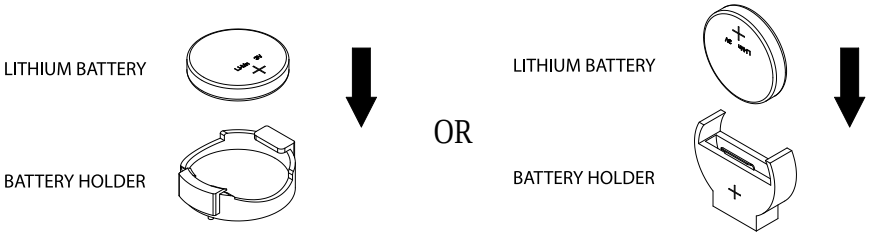
Proper Battery Disposal

Please handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

Battery Installation

1. To install an onboard battery, follow the steps 1 & 2 above and continue below:
2. Identify the battery's polarity. The positive (+) side should be facing up.
3. Insert the battery into the battery holder and push it down until you hear a click to ensure that the battery is securely locked.

Important: When replacing a battery, be sure to only replace it with the same type.



3.5 Returning Merchandise for Service

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. When returning to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton and mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

For faster service, RMA authorizations may be requested online (http://www.supermicro.com/support/rma/).

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alteration, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.

Chapter 4

UEFI BIOS

4.1 Introduction

This chapter describes the AMIBIOS™ Setup utility for the motherboard. The BIOS is stored on a chip and can be easily upgraded using a flash program.

Note: Due to periodic changes to the BIOS, some settings may have been added or deleted and might not yet be recorded in this manual. Please refer to the Manual Download area of our website for any changes to BIOS that may not be reflected in this manual.

Starting the Setup Utility

To enter the BIOS Setup Utility, hit the <Delete> key while the system is booting-up. (In most cases, the <Delete> key is used to invoke the BIOS setup screen. There are a few cases when other keys are used, such as <F1>, <F2>, etc.) Each main BIOS menu option is described in this manual.

The Main BIOS screen has two main frames. The left frame displays all the options that can be configured. “Grayed-out” options cannot be configured. The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it. (Note that BIOS has default text messages built in. We retain the option to include, omit, or change any of these text messages.) Settings printed in **Bold** are the default values.

A " ▶" indicates a submenu. Highlighting such an item and pressing the <Enter> key will open the list of settings within that submenu.

The BIOS setup utility uses a key-based navigation system called hot keys. Most of these hot keys (<F1>, <F2>, <F3>, <Enter>, <ESC>, <Arrow> keys, etc.) can be used at any time during the setup navigation process.

4.2 Main Setup

When you first enter the AMI BIOS setup utility, you will enter the Main setup screen. You can always return to the Main setup screen by selecting the Main tab on the top of the screen. The Main BIOS setup screen is shown below. The following Main menu features will be displayed:



System Date/System Time

Use this option to change the system date and time. Highlight *System Date* or *System Time* using the arrow keys. Enter new values using the keyboard. Press the <Tab> key or the arrow keys to move between fields. The date must be entered in MM/DD/YYYY format. The time is entered in HH:MM:SS format.

Note: The time is in the 24-hour format. For example, 5:30 P.M. appears as 17:30:00. The date's default value is 01/01/2015 after RTC reset.

Supermicro X11DPL-i

BIOS Version

This feature displays the version of the BIOS ROM used in the system.

Build Date

This feature displays the date when the version of the BIOS ROM used in the system was built.

CPLD Version

This feature displays the version of the CPLD (Complex-Programmable Logical Device) used in the system.

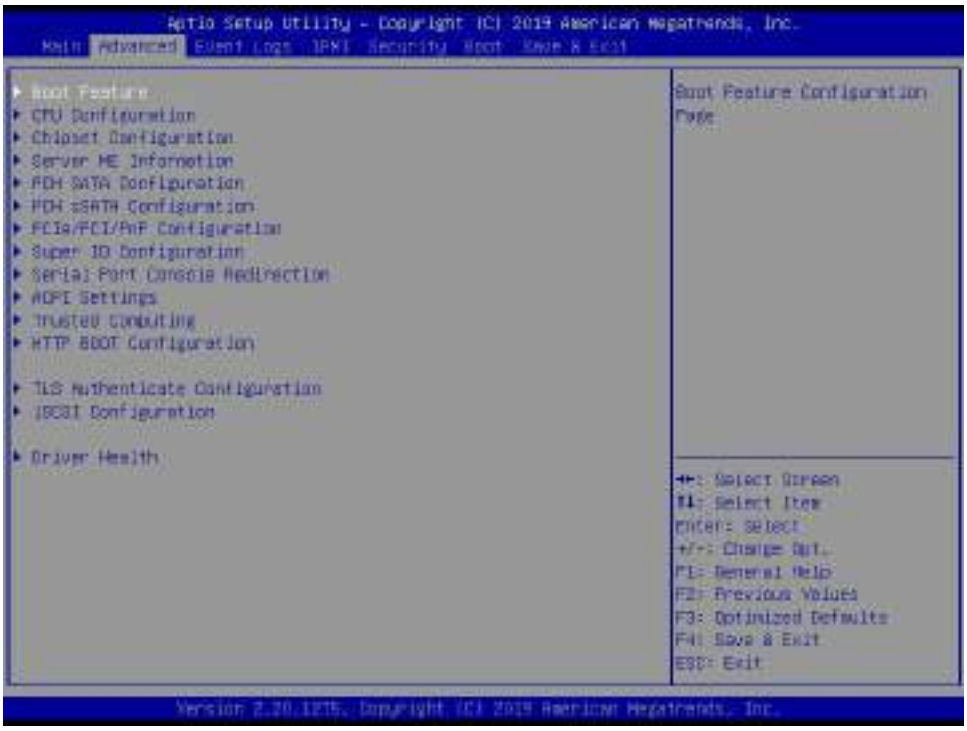
Memory Information

Total Memory

This feature displays the total size of memory available in the system.

4.3 Advanced Setup Configurations

Use the arrow keys to select Boot Setup and press <Enter> to access the submenu features.



Warning: Take caution when changing the Advanced settings. An incorrect value, a very high DRAM frequency, or an incorrect DRAM timing setting may make the system unstable. When this occurs, revert to the default to the manufacture default settings.

▶Boot Feature

Quiet Boot

Use this feature to select the screen display between the POST messages and the OEM logo upon bootup. Select Disabled to display the POST messages. Select Enabled to display the OEM logo instead of the normal POST messages. The options are Disabled and **Enabled**.

Option ROM Messages

Use this feature to set the display mode for the Option ROM. Select Keep Current to display the current Add On ROM setting. Select Force BIOS to use the Option ROM display set by the system BIOS. The options are **Force BIOS** and Keep Current.

Bootup NumLock State

Use this feature to set the Power-on state for the <Numlock> key. The options are **On** and Off.

Wait For "F1" If Error

Use this feature to force the system to wait until the 'F1' key is pressed if an error occurs. The options are Disabled and **Enabled**.

INT19 (Interrupt 19) Trap Response

Interrupt 19 is the software interrupt that handles the boot disk function. When this feature is set to Immediate, the ROM BIOS of the host adaptors will "capture" Interrupt 19 at bootup immediately and allow the drives that are attached to these host adaptors to function as bootable disks. If this feature is set to Postponed, the ROM BIOS of the host adaptors will not capture Interrupt 19 immediately and allow the drives attached to these adaptors to function as bootable devices at bootup. The options are **Immediate** and Postponed.

Re-try Boot

If this feature is enabled, the BIOS will automatically reboot the system from a specified boot device after its initial boot failure. The options are **Disabled**, Legacy Boot, and EFI Boot.

Install Windows 7 USB Support

Enable this feature to use the USB keyboard and mouse during the Windows 7 installation, since the native XHCI driver support is unavailable. Use a SATA optical drive as a USB drive, and USB CD/DVD drives are not supported. Disable this feature after the XHCI driver has been installed in Windows. The options are **Disabled** and Enabled.

Port 61h Bit-4 Emulation

Select Enabled to enable the emulation of Port 61h Bit-4 toggling in SMM (System Management Mode). The options are **Disabled** and Enabled.

Power Configuration

Watch Dog Function

If enabled, the Watch Dog Timer will allow the system to reset or generate NMI based on jumper settings when it is expired for more than five minutes. The options are **Disabled** and Enabled**.**

Restore on AC Power Loss

Use this feature to set the power state after a power outage. Select Stay Off for the system power to remain off after a power loss. Select Power On for the system power to be turned on after a power loss. Select Last State to allow the system to resume its last power state before a power loss. The options are Stay Off, Power On, and **Last State**.

Power Button Function

This feature controls how the system shuts down when the power button is pressed. Select 4 Seconds Override for the user to power off the system after pressing and holding the power button for 4 seconds or longer. Select Instant Off to instantly power off the system as soon as the user presses the power button. The options are **Instant Off** and 4 Seconds Override**.**

▶CPU Configuration

Processor Configuration

The following CPU information will display:

- Processor BSP Revision
- Processor Socket
- Processor ID
- Processor Frequency
- Processor Max Ratio
- Processor Min Ratio
- Microcode Revision
- L1 Cache RAM
- L2 Cache RAM
- L3 Cache RAM
- Processor 0 Version
- Intel(R) Xeon (R) Platinum 8253 CPU @ 2.20GHz
- Processor 1 Version
- Intel(R) Xeon (R) Platinum 8253 CPU @ 2.10GHz

Hyper-Threading (ALL) (Available when supported by the CPU)

Select Enable to support Intel Hyper-threading Technology to enhance CPU performance. The options are Disable and **Enable**.

Cores Enabled

Use this feature to enable or disable CPU cores in the processor specified by the user. The default setting is **0**.

Monitor/MWAIT

Select Enable to enable the Monitor/Mwait instructions in the processor. The options are **Auto**, Enable and Disable.

Execute Disable Bit (Available if supported by the OS & the CPU)

Select Enabled to enable the Execute-Disable Bit which will allow the processor to designate areas in the system memory where an application code can execute and where it cannot, thus preventing a worm or a virus from flooding illegal codes to overwhelm the processor or damage the system during an attack. The default is **Enable**. (Refer to the Intel® and Microsoft® websites for more information.)

Intel Virtualization Technology

Use feature to enable the Vanderpool Technology. This technology allows the system to run several operating systems simultaneously. The options are Disable and **Enable**.

PPIN Control

Select Unlock/Enable to use the Protected-Processor Inventory Number (PPIN) in the system. The options are Unlock/Disable and **Unlock/Enable**.

Hardware Prefetcher (Available when supported by the CPU)

If this feature is set to Enabled, the hardware prefetcher will prefetch streams of data and instructions from the main memory to the L2 cache to improve CPU performance. The options are **Enable** and Disable.

Adjacent Cache Prefetch (Available when supported by the CPU)

The CPU prefetches the cache line for 64 bytes if this feature is set to Disabled. The CPU prefetches both cache lines for 128 bytes as comprised if this feature is set to **Enable**.

DCU Streamer Prefetcher (Available when supported by the CPU)

Select Enabled to enable the DCU (Data Cache Unit) Streamer Prefetcher which will stream and prefetch data and send it to the Level 1 data cache to improve data processing and system performance. The options are **Enable** and Disable.

DCU IP Prefetcher (Available when supported by the CPU)

Select Enabled for DCU (Data Cache Unit) IP Prefetcher support, which will prefetch IP addresses to improve network connectivity and system performance. The options are **Enable** and Disable.

LLC Prefetch

If this feature is set to Enabled, the hardware prefetcher will prefetch streams of data and instructions from the main memory to the L3 cache to improve CPU performance. The options are **Disable** and Enable.

Extended APIC

Select Enable to activate APIC (Advanced Programmable Interrupt Controller) support. The options are **Disable** and Enable.

AES-NI

Select Enable to use the Intel Advanced Encryption Standard (AES) New Instructions (NI) to ensure data security. The options are Disable and **Enable**.

▶Advanced Power Management Configuration

Power Technology

Select Energy Efficient to support power-saving mode. Select Custom to customize system power settings. Select Disabled to disable power-saving settings. The options are Disable, **Energy Efficient**, and Custom.

If the feature above is set to Custom, the following features will become available for user's configuration

Power Performance Tuning (Available when "Power Technology" is set to Custom)

Select BIOS to allow the system BIOS to configure the Power-Performance Tuning Bias setting below. The options are BIOS Controls EPB and **OS Controls EPB**.

ENERGY_PERF-BIAS-CFG mode (ENERGY PERFORMANCE BIAS CONFIGURATION Mode) (Available when supported by the Processor and when "Power Performance Tuning" is set to BIOS Controls EPB)

Use this feature to set the processor power use policy to achieve the desired operation settings for your machine by prioritizing system performance or energy savings. Select Maximum Performance to maximize system performance (to its highest potential); however, this may result in maximum power consumption as energy is needed to fuel the processor frequency. The higher the performance is, the higher the power consumption will be. Select Max Power Efficient to maximize power saving; however, system performance may be substantially impacted because limited power use decreases the processor frequency. The options are Maximum Performance, Performance, **Balanced Performance**, Balanced Power, and Power.

▶CPU P State Control (Available when "Power Technology" is set to Custom)

This feature allows the user to configure the following CPU power settings.

Speedstep (P-States)

Intel SpeedStep Technology allows the system to automatically adjust processor voltage and core frequency to reduce power consumption and heat dissipation. The options are Disable and **Enable**.

EIST PSD Function

This feature allows the user to choose between Hardware and Software to control the processor's frequency and performance (P-state). In HW_ALL mode, the processor hardware is responsible for coordinating the P-state, and the OS is responsible for keeping the P-state request up to date on all logical processors. In SW_ALL mode, the OS Power Manager is responsible for coordinating the P-state, and must initiate the transition on all Logical Processors. In SW_ANY mode, the OS Power Manager is responsible for coordinating the P-state and may initiate the transition on any Logical Processors. The options are **HW_ALL**, SW_ALL and SW-ANY.

Turbo Mode (Available when "SpeedStep" is set to Enable)

Select Enable for processor cores to run faster than the frequency specified by the manufacturer. The options are Disable and **Enable**.

▶Hardware PM (Power Management) State Control (Available when "Power Technology" is set to Custom)

Hardware P-States

If this feature is set to Disable, hardware will choose a P-state setting for the system based on an OS request. If this feature is set to Native Mode, hardware will choose a P-state setting based on OS guidance. If this feature is set to Native Mode with No Legacy Support, hardware will choose a P-state setting independently without OS guidance. The options are **Disable**, Native Mode, Out of Band Mode, and Native Mode with No Legacy Support.

▶CPU C State Control (Available when "Power Technology" is set to Custom)

Autonomous Core C-State

Select Enable to support Autonomous Core C-State control which will allow the processor core to control its C-State setting automatically and independently. The options are Enable and **Disable**.

CPU C6 Report

Select Enable to allow the BIOS to report the CPU C6 state (ACPI C3) to the operating system. During the CPU C6 state, power to all caches is turned off. The options are **Auto**, Enable, and Disable.

Enhanced Halt State (C1E)

Select Enabled to use Enhanced Halt-State technology, which will significantly reduce the CPU's power consumption by reducing the CPU's clock cycle and voltage during a Halt-state. The options are Disable and **Enable**.

Package C State Control (Available when "Power Technology" is set to Custom)

Package C State

This feature allows the user to set the limit on the C State package register. The options are C0/C1 State, C2 State, C6 (Non Retention) State, C6 (Retention) state, No Limit, and **Auto**.

▶CPU T State Control

Software Controlled T-States

If this feature is set to Enable, CPU throttling settings will be supported by the software of the system. The options are Disable and **Enable**.

▶Chipset Configuration

Warning: Setting the wrong values in the following features may cause the system to malfunction.

▶North Bridge

This feature allows users to configure the following North Bridge settings.

▶UPI Configuration

UPI Configuration

The following UPI information will display:

- Number of CPU
- Number of Active UPI Link
- Current UPI Link Speed
- Current UPI Link Frequency
- UPI Global MMIO Low Base / Limit
- UPI Global MMIO High Base / Limit
- UPI Pci-e Congfiguration Base / Size

Degrade Precedence

Use this feature to set degrade precedence when system settings are in conflict. Select Topology Precedence to degrade Features. Select Feature Precedence to degrade Topology. The options are **Topology Precedence** and Feature Precedence.

Link L0p Enable

Select Enable for Link L0p support. The options are Disable, Enable, and **Auto**.

Link L1 Enable

Select Enable for Link L1 support. The options are Disable, Enable, and **Auto**.

IO Directory Cache (IODC)

IO Directory Cache is an 8-entry cache that stores the directory state of remote IIO writes and memory lookups, and saves directory updates. Use this feature to lower cache to cache (C2C) transfer latencies. The options are Disable, **Auto**, Enable for Remote InvItoM Hybrid Push, InvItoM AllocFlow, Enable for Remote InvItoM Hybrid AllocNonAlloc, and Enable for Remote InvItoM and Remote WViLF.

SNC

Select Enable to use the "Sub NUMA (Non-Uniform Memory Access) Cluster" (SNC) memory scheme, which supports full SNC (2-cluster) interleave and 1-way IMC interleave. Select Auto for 1-cluster or 2-cluster support depending on the status of IMC (Integrated Memory Controller) Interleaving. The options are **Disable**, Enable, and Auto.

XPT Prefetch

Select Enable for Extended (Xtended) Prediction Table (XPT) Prefetch support which will allow a read request to be sent to the memory controller requesting the prefetch in parallel to an LLC (Last Level Cache) look-up. The options are **Disable** and Enable.

KTI Prefetch

KTI Prefetch is a feature that enables memory read to start early on a DDR bus, where the KTI Rx path will directly create a Memory Speculative Read command to the memory controller. The options are **Enable** and Disable.

Local/Remote Threshold

Use this feature to configure the threshold settings for local and remote systems that are connected in the network. The options are Disable, **Auto**, Low, Medium, and High.

Stale AtoS

Select Enable to remove the contents and the structures of the files that are no longer needed in the remote host server but are still in use by the local client machine from Directory A to Directory S in the NFS (Network File System) to optimize system performance. The options are Disable, Enable, and **Auto**.

LLC Dead Line Alloc

Select Enable to opportunistically fill the deadlines in LLC (Last Level Cache). The options are Disable, **Enable**, and Auto.

Isoc Mode

Select Enabled for Isochronous support to meet QoS (Quality of Service) requirements. This feature is especially important for Virtualization Technology. The options are Disable, Enable, and **Auto**.

▶Memory Configuration

Integrated Memory Controller (IMC)

Enforce POR

Select Enable to enforce POR restrictions on DDR4 frequency and voltage programming. The options are **POR** and Disable.

PPR Type

Post Package Repair (PPR) is a new feature available for the DDR4 Technology. PPR provides additional spare capacity within a DDR4 DRAM module that is used to replace faulty cell areas detected during system boot. PPR offers two types of memory repairs. Soft Post Package Repair (sPPR) provides a quick, temporary fix on a raw element in a bank group of a DDR4 DRAM device, while hard Post Package Repair (hPPR) will take a longer time to provide permanent repair on a raw element. The options are **Auto**, Hard PPR, Soft PPR, and PPR Disabled.

Memory Frequency

Use this feature to set the maximum memory frequency for onboard memory modules. The options are **Auto**, 1866, 2000, 2133, 2400, 2666, and 2933. **Note:** 2933 MHz memory is supported by 2nd Gen Intel Xeon Scalable-SP (82xx/62xx series) processors only.

Data Scrambling for DDR4

Use this feature to enable or disable data scrambling for DDR4 memory. The options are **Auto**, Disable, and Enable.

tCCD_L Relaxation

If Enabled, the tCCD_L overrides the SPD. When disabled, it is enforced based on memory frequency. The options are Disable and **Auto**.

2X REFRESH

This option allows the user to select 2X refresh mode. The options are **Auto** and Enabled.

Page Policy

Use this feature to set the page policy for onboard memory support The options are **Auto**, Closed, and Adaptive.

IMC Interleaving

Use this feature to configure interleaving settings for the IMC (Integrated Memory Controller), which will improve memory performance. The options are **Auto**, 1-way Interleave, and 2-way Interleave.

▶Memory Topology

This feature displays DIMM population information.

P1 DIMMA1: 2934MT/S SK Hynix SRx8 8GB RDIMM

P2 DIMMA1: 2934MT/S SK Hynix SRx8 8GB RDIMM

▶Memory RAS Configuration

Memory RAS Configuration Setup

Static Virtual Lockstep Mode

Select Enable to run the system's memory channels in lockstep mode to minimize memory access latency. The options are **Disable** and Enable.

Mirror Mode

This feature allows memory to be mirrored between two channels, providing 100% redundancy. The options are **Disable**, Mirror Mode 1LM, and Mirror Mode 2LM.

Memory Rank Sparing

Select Enable to enable memory-sparing support for memory ranks to improve memory performance. The options are **Disable** and Enable.

Correctable Error Threshold

Use this feature to specify the threshold value for correctable memory-error logging, which sets a limit on the maximum number of events that can be logged in the memory-error log at a given time. The default setting is **100**.

Intel Run Sure

Select Enable to support Intel® Run Sure Technology to further enhance critical data protection and to increase system uptime and resiliency. The options are **Disable** and Enable.

Patrol Scrub

Patrol Scrubbing is a process that allows the CPU to correct correctable memory errors detected on a memory module and send the correction to the requestor (the original source). When this feature is set to Enable, the IO hub will read and write back one cache line every 16K cycles, if there is no delay caused by internal processing. By using this method, roughly 64 GB of memory behind the IO hub will be scrubbed every day. The options are Disable and **Enable**.

Patrol Scrub Interval

This feature allows you to decide how many hours the system should wait before the next complete patrol scrub is performed. Use the keyboard to enter a value from 0-24. The default setting is **24**.

▶IIO Configuration

EV DFX Features

When this feature is set to Enable, the EV_DFX Lock Bits that are located in a processor will always remain clear during electric tuning. The options are **Disable** and Enable.

▶CPU1 Configuration

IOU0 (II0 PCIe Br1)

This feature configures the PCI-E port Bifurcation setting for a PCI-E port specified by the user. The options are x4x4x4x4, x4x4x8, x8x4x4, x8x8, x16, and **Auto**.

IOU1 (II0 PCIe Br2)

This feature configures the PCI-E port Bifurcation setting for a PCI-E port specified by the user. The options are x4x4x4x4, x4x4x8, x8x4x4, x8x8, x16, and **Auto**.

IOU2 (II0 PCIe Br3)

This feature configures the PCI-E port Bifurcation setting for a PCI-E port specified by the user. The options are x4x4x4x4, x4x4x8, x8x4x4, x8x8, x16, and **Auto**.

▶CPU1 SLOT4 PCI-E 3.0 x8

Link Speed

Use this feature to select the link speed for the PCI-E port specified by the user. The options are **Auto,** Gen 1 (2.5 GT/s), Gen 2 (5 GT/s), and Gen 3 (8 GT/s).

PCI-E Port Link Status

This feature displays current PCI-E Link Status.

PCI-E Port Link Max

This feature displays PCI-E Link maximum value.

PCI-E Port Link Speed

This feature displays current PCI-E Link Speed.

PCI-E Port Max Payload Size

Selecting **Auto** for this feature will enable the motherboard to automatically detect the maximum Transaction Layer Packet (TLP) size for the connected PCI-E device, allowing for maximum I/O efficiency. Selecting 128B or 256B will designate maximum packet size of 128 or 256. Options are **Auto**, 128B, and 256B. Auto is enabled by default.

▶CPU2 Configuration

IOU2 (II0 PCIe Br3)

This feature configures the PCI-E port Bifurcation setting for a PCI-E port specified by the user. The options are x4x4x4x4, x4x4x8, x8x4x4, x8x8, x16, and **Auto.**

▶IOAT Configuration

Disable TPH

Transparent Hugepages is a Linux memory management system that enables communication in larger blocks (pages). Enabling this feature will increase performance. The options are **No** and Yes.

Prioritize TPH

Use this feature to enable Prioritize TPH support. The options are Enable and **Disable**.

Relaxed Ordering

Select Enable to enable Relaxed Ordering support which will allow certain transactions to violate the strict-ordering rules of PCI bus for a transaction to be completed prior to other transactions that have already been enqueued. The options are **Disable** and Enable.

▶Intel® VT for Directed I/O (VT-d)

Intel® VT for Directed I/O (VT-d)

Select Enable to use Intel Virtualization Technology for Direct I/O VT-d support by reporting the I/O device assignments to the VMM (Virtual Machine Monitor) through the DMAR ACPI tables. This feature offers fully-protected I/O resource sharing across Intel platforms, providing greater reliability, security and availability in networking and data-sharing. The options are **Enabled** and Disabled.

ACS Control

This feature allows users to choose whether they want to enable or disable PCIe Access Control Services (ACS) Extended Capability. The options are **Enabled** and Disabled.

Interrupt Remapping

Select Enable for Interrupt Remapping support to enhance system performance. The options are **Enable** and Disable.

Pass Through DMA

Use this feature to allow devices such as network cards to access the system memory without using a processor. Select Enable to use the Non-Isoch VT_D Engine Pass Through Direct Memory Access (DMA) support. The options are **Enable** and Disable.

ATS

Use this feature to enable Non-Isoch VT-d Engine Address Translation Services (ATS) support. ATS translates virtual addresses to physical addresses. The options are **Enable** and Disable.

Posted Interrupt

Use this feature to enable VT_D Posted Interrupt. The options are **Enable** and Disable.

Coherency Support (Non-Isoch)

Use this feature to maintain setting coherency between processors or other devices. Select Enable for the Non-Iscoh VT-d engine to pass through DMA to enhance system performance. The options are **Enable** and Disable.

▶Intel® VMD Technology

This section describes the configuration settings for the Intel Volume Management Device (VMD) Technology.

Note: After you've enabled VMD on a PCI-E slot of your choice, this PCI-E slot will be dedicated for VMD use only, and it will no longer support any PCI-E device. To re-activate this slot for PCI-E use, please disable VMD.

▶Intel® VMD for Volume Management Device on CPU1

VMD Config for PStack0

Intel® VMD for Volume Management Device for PStack0

Select Enable to use the Intel Volume Management Device Technology for this stack. The options are **Disable** and Enable.

If the feature "Intel VMD for Volume Management Device for PStack0" above is set to Enable, the following features will become available for user's configuration:

CPU SLOT6 PCI-E 3.0 X8 VMD (Available when the device is detected by the system)

Select Enable to use the Intel Volume Management Device Technology for this specific root port. The options are **Disable** and Enable.

CPU SLOT4 PCI-E 3.0 X8 VMD (Available when the device is detected by the system)

Select Enable to use the Intel Volume Management Device Technology for this specific root port. The options are **Disable** and Enable.

Hot Plug Capable (Available when the device is detected by the system)

Use this feature to enable hot plug support for PCIe root ports 1A~1D. The options are **Disable** and Enable.

VMD Config for PStack1

Intel® VMD for Volume Management Device for PStack1

Select Enable to use the Intel Volume Management Device Technology for this stack. The options are **Disable** and Enable.

If the feature "Intel VMD for Volume Management Device for PStack1" above is set to Enable, the following features will become available for user's configuration:

CPU SLOT5 PCI-E 3.0 X16 VMD (Available when the device is detected by the system

Select Enable to use the Intel Volume Management Device Technology for this specific root port. The options are **Disable** and Enable.

Hot Plug Capable (Available when the device is detected by the system)

Use this feature to enable hot plug support for PCIe root ports 2A~2D. The options are **Disable** and Enable.

VMD Config for PStack2

Intel® VMD for Volume Management Device for PStack2

Select Enable to use the Intel Volume Management Device Technology for this stack. The options are **Disable** and Enable.

If the feature "Intel VMD for Volume Management Device for PStack2" above is set to Enable, the following features will become available for user's configuration:

CPU SLOT1 PCI-E 3.0 X4 (IN x8) VMD (Available when the device is detected by the system)

Select Enable to use the Intel Volume Management Device Technology for this specific root port. The options are **Disable** and Enable

CPU SLOT2 PCI-E 3.0 X8 VMD (Available when the device is detected by the system)

Select Enable to use the Intel Volume Management Device Technology for this specific root port. The options are **Disable** and Enable.

Hot Plug Capable (Available when the device is detected by the system)

Use this feature to enable hot plug support for PCIe root ports 3A~3D. This will allow the user to replace the components without shutting down the system. The options are **Disable** and Enable.

▶Intel® VMD for Volume Management Device on CPU2

VMD Config for PStack2

Intel® VMD for Volume Management Device for PStack2

Select Enable to use the Intel Volume Management Device Technology for this stack. The options are **Disable** and Enable.

If the feature "Intel VMD for Volume Management Device for PStack2" above is set to Enable, the following features will become available for user's configuration:

CPU SLOT3 PCI-E 3.0 X16 VMD (Available when the device is detected by the system

Select Enable to use the Intel Volume Management Device Technology for this specific root port. The options are **Disable** and Enable.

Hot Plug Capable (Available when the device is detected by the system)

Use this feature to enable hot plug support for PCIe root ports 3A~3D. The options are **Disable** and Enable.

PCIE Express Global Options

PCI-E Completion Timeout Disable

Use this feature to enable PCI-E Completion Timeout support for electric tuning. The options are Yes, **No**, and Per-Port.

▶South Bridge

USB Module Version

This feature display current USB module version.

USB Devices

This feature display current USB device.

Legacy USB Support

This feature enables support for USB 2.0 and older. The options are **Enabled,** Disabled, and Auto. Default setting is **Enabled**.

XHCI Hand-off

This is a work-around solution for operating systems that do not support XHCI (Extensible Host Controller Interface) hand-off. The XHCI ownership changes should be claimed by the XHCI driver. The options are Enabled and **Disabled**.

Port 60/64 Emulation

Select Enabled for legacy I/O support for USB devices such as mice and keyboards. The options are **Enabled** and disabled. Default setting is **Enabled**.

PCIe PLL SSC

Select Enable for PCH PCIe Spread Spectrum Clocking support, which will allow the BIOS to monitor and attempt to reduce the level of Electromagnetic Interference caused by the components whenever needed. The options are **Disable** and Enable.

▶Server ME (Management Engine) Configuration

This feature displays the following system ME configuration settings.

- General ME Configuration
- Oper. Firmware Version
- Backup Firmware Version
- Recovery Firmware Version
- ME Firmware Status #1
- ME Firmware Status #2
- Current State
- Error Code

▶PCH SATA Configuration

When this submenu is selected, the AMI BIOS automatically detects the presence of the SATA devices that are supported by the Intel PCH chip and displays the following features:

SATA Controller

This feature enables or disables the onboard SATA controller supported by the Intel PCH chip. The options are Disable and **Enable**.

Configure SATA as

Select IDE to configure a SATA drive specified by the user as an IDE drive. Select AHCI to configure a SATA drive specified by the user as an AHCI drive. Select RAID to configure a SATA drive specified by the user as a RAID drive. The options are **AHCI** and RAID.

SATA HDD Unlock

This feature allows the user to remove any password-protected SATA disk drives. The options are Disable and **Enable**.

Aggressive Link Power Management

When this feature is set to Enabled, the SATA AHCI controller manages the power usage of the SATA link. The controller will put the link in a low power mode during extended periods of I/O inactivity, and will return the link to an active state when I/O activity resumes. The options are **Disabled** and Enabled.

If the feature above "Configure SATA as" is set to RAID, the following features will become available for user's configuration:

SATA RAID Option ROM/UEFI Driver

Use this feature to select the Raid Option ROM type. The options are Disable, EFI, and **Legacy**.

SATA Port 0 ~ Port 7

This feature displays the information detected on the installed SATA drive on the particular SATA port.

- Model number of drive and capacity
- Software Preserve Support

Port 0 ~ Port 7 Hot Plug

Set this feature to Enabled for hot-plugging support, which will allow the user to replace a SATA drive without shutting down the system. The options are Disabled and **Enabled**.

Port 0 ~ Port 7 Spin Up Device

On an edge detect from 0 to 1, set this feature to allow the PCH to initialize the device. The options are **Disabled** and Enabled.

Port 0 ~ Port 7 SATA Device Type

Use this feature to specify if the SATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are **Hard Disk Drive** and Solid State Drive.

▶PCH sSATA Configuration

When this submenu is selected, the AMI BIOS automatically detects the presence of the SATA devices that are supported by the Intel PCH chip and displays the following features:

sSATA Controller

This feature enables or disables the onboard sSATA controller supported by the Intel PCH chip. The options are Disable and **Enable**.

Configure sSATA as

Select AHCI to configure an sSATA drive specified by the user as an AHCI drive. Select RAID to configure an sSATA drive specified by the user as a RAID drive. The options are **AHCI**, and RAID.

SATA HDD Unlock

This feature allows the user to remove any password-protected SATA disk drives. The options are Disable and **Enable**.

Aggressive Link Power Management

When this feature is set to Enabled, the SATA AHCI controller manages the power usage of the SATA link. The controller will put the link in a low power mode during extended periods of I/O inactivity, and will return the link to an active state when I/O activity resumes. The options are **Disabled** and Enabled.

If the feature above "Configure SATA as" is set to RAID, the following features will become available for user's configuration:

sSATA RAID Option ROM/UEFI Driver

Use this feature to select the Raid Option ROM type The options are Diable, EFI, and **Legacy**.

sSATA Port 0 ~ Port 2

This feature displays the information detected on the installed sSATA drive on the particular sSATA port.

Port 0 ~ Port 2 Hot Plug

Set this feature to Enabled for hot-plugging support, which will allow the user to replace a SATA drive without shutting down the system. The options are Disabled and **Enabled**.

Port 0 ~ Port 2 Spin Up Device

On an edge detect from 0 to 1, set this feature to allow the PCH to initialize the device. The options are **Disabled** and Enabled.

Port 0 ~ Port 2 sSATA Device Type

Use this feature to specify if the SATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are **Hard Disk Drive** and Solid State Drive.

▶PCIe/PCI/PnP Configuration

The following information will display:

- PCI Bus Driver Version
- PCI Devices Common Settings:

Above 4G Decoding (Available if the system supports 64-bit PCI decoding)

Select Enabled to decode a PCI device that supports 64-bit in the space above 4G Address. The options are Disabled and **Enabled**.

SR-IOV Support

Use this feature to enable or disable Single Root IO Virtualization Support. The options are **Disabled** and Enabled.

MMIO High Base

Use this feature to select the base memory size according to memory-address mapping for the IO hub. The options are **56T**, 40T, 24T, 16T, 4T, 2T, and 1T.

MMIO High Granularity Size

Use this feature to select the high memory size according to memory-address mapping for the IO hub. The options are 1G, 4G, 16G, 64G, **256G**, and 1024G.

Maximum Read Request

Select Auto for the system BIOS to automatically set the maximum size for a read request for a PCI-E device to enhance system performance. The options are **Auto**, 128 Bytes, 256 Bytes, 512 Bytes, 1024 Bytes, 2048 Bytes, and 4096 Bytes.

MMCFG Base

Use this feature to select the low base address for PCIE adapters to increase base memory. The options are 1G, 1.5G, 1.75G, **2G**, 2.25G. and 3G.

NVMe Firmware Source

Use this feature to select the NVMe firmware to support booting. The options are Vendor Defined Firmware and AMI Native Support. The default option, **Vendor Defined Firmware**, is pre-installed on the drive and may resolve errata or enable innovative functions for the drive. The other option, AMI Native Support, is offered by the BIOS with a generic method.

VGA Priority

Use this feature to select the graphics device to be used as the primary video display for system boot. The options are **Onboard** and Offboard.

CPU1 SLOT1 PCI-E 3.0 X4 (IN x8) OPROM

Select Enabled to enable Option ROM support to boot the computer using a device installed on the slot specified by the user. The options are Disabled, Legacy and EFI. Use this feature to select which firmware type to be loaded for the add-on card in this slot. The options are Disabled, **Legacy**, and EFI.

CPU1 SLOT2 PCI-E 3.0 X8 OPROM

Use this feature to select which firmware type to be loaded for the add-on card in this slot. The options are Disabled, **Legacy**, and EFI.

CPU2 SLOT3 PCI-E 3.0 X16 OPROM

Use this feature to select which firmware type to be loaded for the add-on card in this slot. The options are Disabled, **Legacy**, and EFI.

CPU1 SLOT4 PCI-E 3.0 X8 OPROM

Use this feature to select which firmware type to be loaded for the add-on card in this slot. The options are Disabled, **Legacy**, and EFI.

CPU1 SLOT5 PCI-E 3.0 X16 OPROM

Use this feature to select which firmware type to be loaded for the add-on card in this slot. The options are Disabled, **Legacy**, and EFI.

CPU1 SLOT6 PCI-E 3.0 X8 OPROM

Use this feature to select which firmware type to be loaded for the add-on card in this slot. The options are Disabled, **Legacy**, and EFI.

PCH M.2 PCI-E 3.0 X4 OPROM

Use this feature to select which firmware type to be loaded for the add-on card in this slot. The options are Disabled, **Legacy**, and EFI.

Bus Master Enable

This feature allows users to change Bus Master Enable policy. If Disabled is selected, this policy will be enable based on device settings; if Enabled is selected, the policy will be enabled all the time. The options are **Enabled** and Disabled.

Onboard LAN Device

Select Enabled to enable the Onbaord LAN device. The options are Disabled and **Enabled**.

Onboard LAN1 Option ROM

Use this feature to select which firmware function to be loaded for LAN Port1 used for system boot. The options are Disabled, **Legacy**, and EFI.

Onboard LAN2 Option ROM

Use this feature to select which firmware function to be loaded for LAN Port2 used for system boot. The options are **Disabled**, Legacy, and EFI.

Onboard Video Option ROM

Use this feature to select the Onboard Video Option ROM type. The options are Disabled, **Legacy,** and EFI.

▶Network Stack Configuration

Network Stack

Select Enabled to enable PXE (Preboot Execution Environment) or UEFI (Unified Extensible Firmware Interface) for network stack support. The options are Disabled and **Enabled** .

IPv4 PXE Support

Select Enabled to enable IPv4 PXE boot support. The options are Disabled and **Enabled**.

IPv4 HTTP Support

Select Enabled to enable IPv4 HTTP boot support. The options are **Disabled** and Enabled.

IPv6 PXE Support

Select Enabled to enable IPv6 PXE boot support. The options are Disabled and **Enabled**.

IPv6 HTTP Support

Select Enabled to enable IPv6 HTTP boot support. The options are **Disabled** and Enabled.

PXE Boot Wait Time

Use this option to specify the wait time to press the ESC key to abort the PXE boot. Press "+" or "-" on your keyboard to change the value. The default setting is **0**.

Media Detect Count

Use this option to specify the number of times media will be checked. Press "+" or "-" on your keyboard to change the value. The default setting is **1**.

▶Super IO Configuration

The following Super IO information will display:

- Super IO Chip AST2500

▶Serial Port 1 Configuration

This submenu allows the user the configure settings of Serial Port 1.

Serial Port 1

Select Enabled to enable the selected onboard serial port. The options are Disabled and **Enabled**.

Device Settings

This feature displays the status of a serial part specified by the user.

Change Settings

This feature specifies the base I/O port address and the Interrupt Request address of a serial port specified by the user. Select Auto to allow the BIOS to automatically assign the base I/O and IRQ address. The options for Serial Port 1 are **Auto**, (IO=3F8h; IRQ=4;), (IO=2F8h; IRQ= 4;), (IO=3E8h; IRQ=4;), and (IO=2E8h; IRQ=4;).

▶SOL Configuration

This submenu allows the user to configure the settings of Serial Port 2.

SOL

Select Enabled to enable the selected onboard serial port. The options are Disabled and **Enabled**.

Device Settings

This feature displays the status of a serial part specified by the user.

Change Settings

This feature specifies the base I/O port address and the Interrupt Request address of a serial port specified by the user. Select Auto to allow the BIOS to automatically assign the base I/O and IRQ address. The options for Serial Port 2 are **Auto**, (IO=2F8h; IRQ=3;), (IO=3F8h; IRQ=3;), (IO=3E8h; IRQ=3;), and (IO=2E8h; IRQ=3).

SOL Attribute

Select SOL to use COM Port 2 as a Serial Over LAN (SOL) port for console. The options are **SOL** and COM.

▶Serial Port Console Redirection

COM1

Console Redirection

Select Enabled to enable console redirection support for a serial port specified by the user. The options are **Disabled** and Enabled.

If the feature above is set to Enabled, the following features will become available for user's configuration:

▶Console Redirection Settings

This feature allows the user to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

Terminal Type

This feature allows the user to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, **VT100+**, VT-UTF8, and ANSI.

Bits per second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600 and **115200** (bits per second).

Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 and **8** Bits.

Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark, and Space.

Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and 2.

Flow Control

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are Disabled and **Enabled.**

Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

Legacy OS Redirection Resolution

Use this feature to select the number of rows and columns used in Console Redirection for legacy OS support. The options are 80x24 and **80x25**.

Putty KeyPad

This feature selects the settings for Function Keys and KeyPad used for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SC0, ESCN, and VT400.

Redirection After BIOS POST

Use this feature to enable or disable legacy console redirection after BIOS POST. When set to Bootloader, legacy console redirection is disabled before booting the OS. When set to Always Enable, legacy console redirection remains enabled when booting the OS. The options are **Always Enable** and Bootloader.

SOL

Console Redirection

Select Enabled to enable console redirection support for a serial port specified by the user. The options are Disabled and **Enabled**.

▶Console Redirection Settings

This feature allows the user to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

If the feature above is set to Enabled, the following features will become available for user's configuration:

Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, **VT100+**, VT-UTF8, and ANSI.

Bits Per Second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600 and **115200** (bits per second).

Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 and **8** Bits.

Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark and Space.

Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and 2.

Flow Control

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are Disabled and **Enabled.**

Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

Legacy OS Redirection Resolution

Use this feature to select the number of rows and columns used in Console Redirection for legacy OS support. The options are 80x24 and **80x25**.

Putty KeyPad

This feature selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SCO, ESCN, and VT400.

Redirection After BIOS POST

Use this feature to enable or disable legacy Console Redirection after BIOS POST. When set to Bootloader, legacy Console Redirection is disabled before booting the OS. When set to Always Enable, legacy Console Redirection remains enabled when booting the OS. The options are **Always Enable** and Bootloader.

Legacy Console Redirection

Legacy Serial Redirection Port

Use this feature to select a COM port to display redirection of Legacy OS and Legacy OPROM messages. The options are **COM1** and SOL.

Serial Port for Out-of-Band Management/Windows Emergency Management Services (EMS)

The submenu allows the user to configure Console Redirection settings to support Out-of-Band Serial Port management.

Console Redirection

Select Enabled to use a COM port selected by the user for EMS Console Redirection. The options are Disabled and **Enabled**.

▶Console Redirection Settings

This feature allows the user to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

Out-of-Band Mgmt Port

The feature selects a serial port in a client server to be used by the Microsoft Windows Emergency Management Services (EMS) to communicate with a remote host server. The options are **COM1** and SOL.

Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII character set. Select VT100+ to add color and function key support. Select ANSI to use the extended ASCII character set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, VT100+, **VT-UTF8**, and ANSI.

Bits Per Second

This feature sets the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 57600, and **115200** (bits per second).

Flow Control

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None,** Hardware RTS/CTS, and Software Xon/Xoff.

Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 and **8** Bits.

Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark and Space.

Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and 2.

▶ACPI Settings

Numa

This setting enables or disables Non-Uniform Memory Access (NUMA), a feature that improves memory-to-processor communication and performance. The options are **Enabled** or Disabled.

WHEA Support

Select Enabled to support the Windows Hardware Error Architecture (WHEA) platform and provide a common infrastructure for the system to handle hardware errors within the Windows OS environment to reduce system crashes and to enhance system recovery and health monitoring. The options are Disabled and **Enabled**.

High Precision Timer

Select Enabled to activate the High Precision Event Timer (HPET) that produces periodic interrupts at a much higher frequency than a Real-time Clock (RTC) does in synchronizing multimedia streams, providing smooth playback and reducing the dependency on other timestamp calculation devices, such as an x86 RDTSC Instruction embedded in the CPU. The High Performance Event Timer is used to replace the 8254 Programmable Interval Timer. The options are Disabled and **Enabled**.

▶Trusted Computing (Available when a TPM device is installed and detected by the BIOS)

When a TPM (Trusted-Platform Module) device is detected in your machine, the following information will be displayed.

- Security Device Support
- No Security Device Found

Security Device Support

If this feature and the TPM jumper (JPT1) on the motherboard are both enabled, the onboard security (TPM) device will be enabled in the BIOS to enhance data integrity and system security. Please note that the OS will not show the security device. Neither TCG EFI protocol nor INT1A interaction will be made available for use. If you have made changes on the setting on this feature, be sure to reboot the system for the change to take effect. The options are Disable and **Enable**.

▶HTTP BOOT Configuration Settings

Use this feature to confi gure HTTP Boot settings for your system.

HTTP Boot One Time

If this feature is set to Enabled, the system will automatically boot into the HttpBoot setting that has been previously configured when it was powered up the fi rst time. The options are Enabled and **Disabled**.

Input the Description

This feature allows the user to input the description of the boot.

Boot URI

This feature allows the user to boot the system from a network connection.

▶TLS Authenticate Configuration

When this submenu is selected, the following features will become available for user's configuration:

▶Server CA Configuration

▶Enroll Certification

This feature allows the user to enroll the certificate in the system.

▶Enroll Cert (Certification) Using File

This feature allows the user to enroll the security certificate in the system by using a file.

Cert (Certification) GUID (Global Unique Identifier)

This feature displays the GUID for this system.

▶Commit Changes and Exit

Select this feature to keep the changes you have made and exit from the system.

▶Discard Changes and Exit

Select this feature to discard the changes you have made and exit from the system.

▶Delete Certification

If this feature is set to Enable, the certificate enrolled in the system will be deleted. The options are Enable and **Disable**.

▶iSCSI Configuration

iSCSI Initiator Name

This feature allows the user to enter the unique name of the iSCSI Initiator in IQN format. Once the name of the iSCSI Initiator is entered into the system, configure the proper settings for the following features.

▶Add an Attempt

▶Delete Attempts

▶Change Attempt Order

▶Driver Health

▶Intel(R) DCPMM1.0.0.3429 Driver Healthy

This feature displays the information/status of driver installed in the system.

4.4 Event Logs

Use this feature to configure Event Log settings.



▶Enabling/Disabling Options

Enabling/Disabling Options

SMBIOS Event Log

Change this feature to enable or disable all features of the SMBIOS Event Logging during system boot. The options are **Enabled** and Disabled.

Erasing Settings

Erase Event Log

If No is selected, data stored in the event log will not be erased. Select Yes, Next Reset, data in the event log will be erased upon next system reboot. Select Yes, Every Reset, data in the event log will be erased upon every system reboot. The options are **No**, Yes, Next reset, and Yes, Every reset.

When Log is Full

Select Erase Immediately for all messages to be automatically erased from the event log when the event log memory is full. The options are **Do Nothing** and Erase Immediately.

SMBIOS Event Long Standard Settings

Log System Boot Event

This option toggles the System Boot Event logging to enabled or disabled. The options are **Disabled** and Enabled.

MECI

The Multiple Event Count Increment (MECI) counter counts the number of occurences that a duplicate event must happen before the MECI counter is incremented. This is a numeric value. The default value is **1**.

METW

The Multiple Event Time Window (METW) defines number of minutes must pass between duplicate log events before MECI is incremented. This is in minutes, from 0 to 99. The default value is **60**.



Note: All values changed here do not take effect until computer is restarted.

4.5 IPMI

Use this feature to configure Intelligent Platform Management Interface (IPMI) settings.



BMC Firmware Revision

This feature indicates the IPMI firmware revision used in your system.

IPMI Status (Baseboard Management Controller)

This feature indicates the status of the IPMI firmware installed in your system.

▶System Event Log

Enabling/Disabling Options

SEL Components

Select Enabled for all system event logging at bootup. The options are **Enabled** and Disabled.

Erasing Settings

Erase SEL

Select Yes, On next reset to erase all system event logs upon next system reboot. Select Yes, On every reset to erase all system event logs upon each system reboot. Select No to keep all system event logs after each system reboot. The options are **No,** Yes, On next reset, and Yes, On every reset.

When SEL is Full

This feature allows the user to decide what the BIOS should do when the system event log is full. Select Erase Immediately to erase all events in the log when the system event log is full. The options are **Do Nothing** and Erase Immediately.



Note: All values changed here do not take effect until computer is restarted.

▶BMC Network Configuration

BMC Network Configuration

Update IPMI LAN Configuration

Select Yes for the BIOS to implement all IP/MAC address changes at the next system boot. The options are **No** and Yes.

Configure IPV4 support

IPMI LAN Selection

This feature displays the IPMI LAN setting. The default setting is **Failover**.

IPMI Network Link Status

This feature displays the IPMI Network Link status. The default setting is **Dedicated LAN**.

If the feature above is set to Yes, the following feature will become available for user's configuration:

Configuration Address Source

This feature allows the user to select the source of the IP address for this computer. If Static is selected, you will need to know the IP address of this computer and enter it to the system manually in the field. If DHCP is selected, the BIOS will search for a DHCP (Dynamic Host Configuration Protocol) server in the network that is attached to and request the next available IP address for this computer. The options are **DHCP** and Static.

Station IP Address

This feature displays the Station IP address for this computer. This should be in decimal and in dotted quad form (i.e.,10.135.174.29).

Subnet Mask

This feature displays the sub-network that this computer belongs to. The value of each three-digit number separated by dots should not exceed 255.

Station MAC Address

This feature displays the Station MAC address for this computer. Mac addresses are 6 two-digit hexadecimal numbers.

Gateway IP Address

This feature displays the Gateway IP address for this computer. This should be in decimal and in dotted quad form (i.e., 10.135.0.250).

VLAN

This feature displays the virtual LAN settings. The options are **Disable** and Enable.

Configure IPV6 Support

This section displays configuration features for IPV6 support.

IPV6 address status

This section displays status of station IPV6 address to BMC. The default setting is Disabled.

IPV6 Support

Use this feature to enable IPV6 support. The options are **Enabled** and Disabled.

Configuration Address Source

This feature allows the user to select the source of the IP address for this computer. If Static is selected, you will need to know the IP address of this computer and enter it to the system manually in the field. If DHCP is selected, the BIOS will search for a DHCP (Dynamic Host Configuration Protocol) server in the network that is attached to and request the next available IP address for this computer. The options are Static and **DHCP**.

If the feature above is set to Static, the following feature will become available for user's configuration:

- Station IPV6 Address
- Prefix Length
- IPV6 Router1 IP Address

4.6 Security

This menu allows the user to configure the following security settings for the system.



Administrator Password

User Password

Administrator Password

Press Enter to create a new, or change an existing, Administrator password. The length of the password should be from 3 characters to 20 characters long.

Password Check

Select Setup for the system to check for a password at Setup. Select Always for the system to check for a password at bootup or upon entering the BIOS Setup utility. The options are **Setup** and Always.

▶Secure Boot

This section displays the contents of the following secure boot features:

- System Mode
- Vendor Keys
- Secure Boot

Secure Boot

Use this feature to enable secure boot. The options are **Disabled** and Enabled.

Secure Boot Mode

Use this feature to configure Secure Boot variables without authentication. The options are Standard and **Custom**.

CSM Support

Select Enabled to support the EFI Compatibility Support Module (CSM), which provides compatibility support for traditional legacy BIOS for system boot. The options are **Enabled** and Disabled.

▶Key Management

This submenu allows the user to configure the following Key Management settings.

Vendor Keys

Provision Factory Default Keys

Select Enabled to install the default Secure-Boot keys set by the manufacturer. The options are **Disabled** and Enabled.

▶Restore Factory Keys

Select Yes to restore manufacturer default keys used to ensure system security. The options are **Yes** and No.

▶Reset to Setup Mode

Select Yes to reset the system to the Setup Mode. The options are **Yes** and No.

▶Export Secure Boot Variables

This feature is used to copy the NVRAM content of Secure Boot variables to a storage device.

▶Enroll EFI Image

Select this feature and press <Enter> to specify an EFI (Extensible Firmware Interface) image for the system to use when it operates in the Secure Boot mode.

Device Guard Ready

▶Remove 'UEFI CA' from DB

Select Yes to remove UEFI CA from the database. The options are **Yes** and No.

▶Restore DB defaults

Select Yes to restore database variables to the manufacturer default settings. The options are **Yes** and No.

Secure Boot Variable/Size/Keys/Key Source

▶Platform Key (PK)

This feature allows the user to enter and configure a set of values to be used as platform firmware keys for the system. The sizes, keys numbers, and key sources of the platform keys will be indicated as well. Select **Update** to update the platform key.

▶Key Exchange Keys

This feature allows the user to enter and configure a set of values to be used as Key-Exchange-Keys for the system. The sizes, keys numbers, and key sources of the Key-Exchange-Keys will be indicated as well. Select **Update** to update your "Key Exchange Keys". Select Append to append your "Key Exchange Keys".

▶Authorized Signatures

This feature allows the user to enter and configure a set of values to be used as Authorized Signatures for the system. These values also indicate the sizes, keys numbers, and the sources of the authorized signatures. Select **Update** to update your "Authorized Signatures". Select Append to append your "Authorized Signatures". The settings are Details, Export, Update, Append, and Delete.

▶Forbidden Signatures

This feature allows the user to enter and configure a set of values to be used as Forbidden Signatures for the system. These values also indicate sizes, keys numbers, and key sources of the forbidden signatures. Select **Update** to update your "Forbidden Signatures". Select Append to append your "Forbidden Signatures". The settings are Details, Export, Update, Append, and Delete.

▶Authorized TimeStamps

This feature allows the user to set and save the timestamps for the authorized signatures which will indicate the time when these signatures are entered into the system. Select **Update** to update your "Authorized TimeStamps". Select Append to append your "Authorized TimeStamps". The settings are Update and Append.

▶Os Recovery Signatures

This feature allows the user to set and save the authorized signatures used for OS recovery. Select **Update** to update your "OS Recovery Signatures". Select Append to append your "OS Recovery Signatures". The settings are Update and Append.

4.7 Boot

Use this feature to configure Boot Settings:



Boot Configuration

Boot Mode Select

Use this feature to select the type of devices from which the system will boot. The options are Legacy, UEFI (Unified Extensible Firmware Interface), and **Dual.**

Legacy to EFI Support

Select Enabled for the system to boot from an EFI OS when the Legacy OS fails. The options are Enabled and **Disabled**.

Fixed Boot Order Priorities

This feature prioritizes the order of a bootable device from which the system will boot. Press <Enter> on each item sequentially to select devices.

When the item above -"Boot Mode Select" is set to **Dual** (default), the following items will be displayed for user's configuration:

- Boot Option #1 - Boot Option #17

When the item above -"Boot Mode Select" is set to Legacy, the following items will be displayed for configuration:

- Boot Option #1 - Boot Option #8

When the item above -"Boot Mode Select" is set to UEFI, the following items will be displayed for configuration:

- Boot Option #1 - Boot Option #9

▶Delete Boot Option

Use this feature to select a boot device to delete from the boot priority list.

Delete Boot Option

Use this feature to remove an EFI boot option from the boot priority list.

▶UEFI Application Boot Priorities

This feature allows the user to specify which UEFI devices are boot devices.

Boot Option #1

The options are UEFI: Built-in EFI Shell and Disabled.

▶NETWORK Drive BBS Priorities

This feature allows the user to specify which UEFI network drive devices are boot devices.

Boot Option #1

The options are IBA 40-10G Slot 6000 v1060 and Disabled.

4.8 Save & Exit

Select the Exit tab from the BIOS setup utility screen to enter the Exit BIOS Setup screen.



Save Options

Discard Changes and Exit

Select this option to exit from the BIOS setup utility without making any permanent changes to the system configuration and reboot the computer.

Save Changes and Reset

When you have completed the system configuration changes, select this option to leave the BIOS setup utility and reboot the computer for the new system configuration parameters to become effective.

Save Changes

When you have completed the system configuration changes, select this option to save all changes made. This will not reset (reboot) the system.

Discard Changes

Select this option and press Yes to discard all the changes you've made and load previous values.

Default Options

Restore Optimized Defaults

To set this feature, select Restore Optimized Defaults from the Exit menu and press <Enter> to load manufacturer default settings which are intended for maximum system performance but not for maximum stability.

Save As User Defaults

To set this feature, select Save as User Defaults from the Exit menu and press <Enter>. This enables the user to save all changes to the BIOS setup for future use.

Restore User Defaults

To set this feature, select Restore User Defaults from the Exit menu and press <Enter>. Use this feature to retrieve user-defined default settings that were saved previously.

Boot Override

This feature allows the user to override the Boot priorities sequence in the Boot menu, and immediately boot the system with a device specified by the user instead of the one specified in the boot list. This is an one-time override.

Appendix A

BIOS Codes

A.1 BIOS Error POST (Beep) Codes

During the POST (Power-On Self-Test) routines, which are performed each time the system is powered on, errors may occur.

Non-fatal errors are those which, in most cases, allow the system to continue the boot-up process. The error messages normally appear on the screen.

Fatal errors are those which will not allow the system to continue the boot-up procedure. If a fatal error occurs, you should consult with your system manufacturer for possible repairs. The fatal errors are usually communicated through repeated patterns of audible beeps. Each pattern of audible beeps listed below corresponds to its respective error.

BIOS Beep (POST) Codes		
Beep Code	**Error Message**	**Description**
1 beep	Refresh	Circuits have been reset (Ready to power up)
5 short, 1 long	Memory error	No memory detected in system
5 long, 2 short	Display memory read/write error	Video adapter missing or with faulty memory
1 long continuous	System OH	System overheat condition

A.2 Additional BIOS POST Codes

The AMI BIOS supplies additional checkpoint codes, which are documented online at http://www.supermicro.com/support/manuals/ ("AMI BIOS POST Codes User's Guide").

When BIOS performs the Power On Self Test, it writes checkpoint codes to I/O port 0080h. If the computer cannot complete the boot process, a diagnostic card can be attached to the computer to read I/O port 0080h (Supermicro p/n AOC-LPC80-20).

For information on AMI updates, please refer to http://www.ami.com/products/.

Appendix B

Software Installation

B.1 Installing Software Programs

The Supermicro website that contains drivers and utilities for your system is located at http://www.supermicro.com/wftp. Some of these must be installed, such as the chipset driver.

After accessing the product drivers and utilities page, go into the CDR_Images directory and locate the ISO file for your motherboard. Download this file to create a DVD of the drivers and utilities it contains. (You may also use a utility to extract the ISO file if preferred.)

After creating a DVD with the ISO files, insert the disk into the DVD drive on your system and the display shown in Figure B-1 should appear.

Another option is to go to the Supermicro website at http://www.supermicro.com/products/. Find the product page for your motherboard here, where you may download individual drivers and utilities to your hard drive or a USB flash drive and install from there.

Note: Please refer to the documents posted on our website at http://www.supermicro.com/support/manuals/ for additional instructions that may be applicable to your system.



Figure B-1. Driver/Tool Installation Display Screen

Click the icons showing a hand writing on the paper to view the readme files for each item. Click a computer icon to the right of an item to install an item (from top to the bottom) one at a time. After installing each item, you must reboot the system before proceeding with the next item on the list. The bottom icon with a CD on it allows you to view the entire contents of the CD.

When making a storage driver diskette by booting into a driver CD, please set the SATA Configuration to "Compatible Mode" and configure SATA as IDE in the BIOS Setup. After making the driver diskette, be sure to change the SATA settings back to your original settings.

B.2 SuperDoctor® 5

The Supermicro SuperDoctor 5 is a hardware monitoring program that functions in a command-line or web-based interface in Windows and Linux operating systems. The program monitors system health information such as CPU temperature, system voltages, system power consumption, fan speed, and provides alerts via email or Simple Network Management Protocol (SNMP).

SuperDoctor 5 comes in local and remote management versions and can be used with Nagios to maximize your system monitoring needs. With SuperDoctor 5 Management Server (SSM Server), you can remotely control power on/off and reset chassis intrusion for multiple systems with SuperDoctor 5 or IPMI. SD5 Management Server monitors HTTP and SMTP services to optimize the efficiency of your operation.

Note: The default Username and Password for SuperDoctor 5 is ADMIN / ADMIN.

Figure B-2. SuperDoctor 5 Interface Display Screen (Health Information)



Note: The SuperDoctor 5 program and user's manual can be downloaded from the Supermicro website at http://www.supermicro.com/products/nfo/sms_sd5.cfm.

Appendix C

Standardized Warning Statements

The following statements are industry standard warnings, provided to warn the user of situations where bodily injury might occur. Should you have questions or experience difficulty, contact Supermicro's Technical Support department for assistance. Only certified technicians should attempt to install or configure components.

Read this section in its entirety before installing or configuring components.

These warnings may also be found on our website at http://www.supermicro.com/about/policies/safety_information.cfm.

Battery Handling



Warning! There is the danger of explosion if the battery is replaced incorrectly. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions

電池の取り扱い

電池交換が正しく行われなかった場合、破裂の危険性があります。交換する電池はメーカーが推奨する型、または同等のものを使用下さい。使用済電池は製造元の指示に従って処分して下さい。

警告

电池更换不当会有爆炸危险。请只使用同类电池或制造商推荐的功能相当的电池更换原有电池。请按制造商的说明处理废旧电池。

警告

電池更換不當會有爆炸危險。請使用製造商建議之相同或功能相當的電池更換原有電池。請按照製造商的說明指示處理廢棄舊電池。

Warnung

Bei Einsetzen einer falschen Batterie besteht Explosionsgefahr. Ersetzen Sie die Batterie nur durch den gleichen oder vom Hersteller empfohlenen Batterietyp. Entsorgen Sie die benutzten Batterien nach den Anweisungen des Herstellers.

Attention

Danger d'explosion si la pile n'est pas remplacée correctement. Ne la remplacer que par une pile de type semblable ou équivalent, recommandée par le fabricant. Jeter les piles usagées conformément aux instructions du fabricant.

¡Advertencia!

Existe peligro de explosión si la batería se reemplaza de manera incorrecta. Reemplazar la batería exclusivamente con el mismo tipo o el equivalente recomendado por el fabricante. Desechar las baterías gastadas según las instrucciones del fabricante.

אזהרה!

קיימת סכנת פיצוץ של הסוללה במידה והוחלפה בדרך לא תקינה. יש להחליף
את הסוללה בסוג התואם מחברת יצרן מומלצת.
סילוק הסוללות המשומשות יש לבצע לפי הוראות היצרן.

هناك خطر من انفجار في حالة اسحبذال البطارية بطريقة غير صحيحة فعليل
اسحبذال البطارية
فقط بنفس النع أو ما يعادلها مما أوصث به الشرمة المصنعة
جخلص من البطاريات المسحعملة وفقا لحعليمات الشرمة الصانعة

경고!

배터리가 올바르게 교체되지 않으면 폭발의 위험이 있습니다. 기존 배터리와 동일하거나 제조사에서 권장하는 동등한 종류의 배터리로만 교체해야 합니다. 제조사의 안내에 따라 사용된 배터리를 처리하여 주십시오.

Waarschuwing

Er is ontploffingsgevaar indien de batterij verkeerd vervangen wordt. Vervang de batterij slechts met hetzelfde of een equivalent type die door de fabrikant aanbevolen wordt. Gebruikte batterijen dienen overeenkomstig fabrieksvoorschriften afgevoerd te worden.

Product Disposal



Warning! Ultimate disposal of this product should be handled according to all national laws and regulations.

製品の廃棄

この製品を廃棄処分する場合、国の関係する全ての法律・条例に従い処理する必要があります。

警告

本产品的废弃处理应根据所有国家的法律和规章进行。

警告

本產品的廢棄處理應根據所有國家的法律和規章進行。

Warnung

Die Entsorgung dieses Produkts sollte gemäß allen Bestimmungen und Gesetzen des Landes erfolgen.

¡Advertencia!

Al deshacerse por completo de este producto debe seguir todas las leyes y reglamentos nacionales.

Attention

La mise au rebut ou le recyclage de ce produit sont généralement soumis à des lois et/ou directives de respect de l'environnement. Renseignez-vous auprès de l'organisme compétent.

סילוק המוצר

אזהרה!

סילוק סופי של מוצר זה חייב להיות בהתאם להנחיות וחוקי המדינה.

عند التخلص النهائي من هذا المنتج ينبغي التعامل معه وفقا لجميع القانين واللائح البطنية

경고!

이 제품은 해당 국가의 관련 법규 및 규정에 따라 폐기되어야 합니다.

Waarschuwing

De uiteindelijke verwijdering van dit product dient te geschieden in overeenstemming met alle nationale wetten en reglementen.

Appendix D

UEFI BIOS Recovery

Warning: Do not upgrade the BIOS unless your system has a BIOS-related issue. Flashing the wrong BIOS can cause irreparable damage to the system. In no event shall Supermicro be liable for direct, indirect, special, incidental, or consequential damages arising from a BIOS update. If you need to update the BIOS, do not shut down or reset the system while the BIOS is updating to avoid possible boot failure.

D.1 Overview

The Unified Extensible Firmware Interface (UEFI) provides a software-based interface between the operating system and the platform firmware in the pre-boot environment. The UEFI specification supports an architecture-independent mechanism that will allow the UEFI OS loader stored in an add-on card to boot the system. The UEFI offers clean, hands-off management to a computer during system boot.

D.2 Recovering the UEFI BIOS Image

A UEFI BIOS flash chip consists of a recovery BIOS block and a main BIOS block (a main BIOS image). The boot block contains critical BIOS codes, including memory detection and recovery codes for the user to flash a new BIOS image if the original main BIOS image is corrupted. When the system power is first turned on, the boot block codes execute first. Once this process is completed, the main BIOS code will continue with system initialization and the remaining POST (Power-On Self-Test) routines.

Note 1: Follow the BIOS recovery instructions below for BIOS recovery when the main BIOS boot crashes.

Note 2: When the BIOS boot block crashes, you will need to follow the procedures to make a Returned Merchandise Authorization (RMA) request. (For a RMA request, please see section 3.5 for more information). Also, you may use the Supermicro Update Manager (SUM) Out-of-Band (OOB) (https://www.supermicro.com.tw/products/nfo/SMS_SUM.cfm) to reflash the BIOS.

D.3 Recovering the BIOS Block with a USB Device

This feature allows the user to recover a BIOS image using a USB-attached device without additional utilities used. A USB flash device such as a USB Flash Drive, or a USB CD/DVD ROM/RW device can be used for this purpose. However, a USB Hard Disk drive cannot be used for BIOS recovery at this time.

The file system supported by UEFI is FAT (including FAT12, FAT16, and FAT32) which is installed on a bootable or non-bootable USB-attached device. However, the BIOS might need several minutes to locate the SUPER.ROM file if the media size becomes too large due to the huge volumes of folders and files stored in the device.

To perform UEFI BIOS recovery using a USB-attached device, follow the instructions below.

1. Using a different machine, copy the "Super.ROM" binary image file into the disc Root "\" directory of a USB device or a writable CD/DVD.

   Note: If you cannot locate the "Super.ROM" file in your driver disk, visit our website at www.supermicro.com to download the BIOS image into a USB flash device and rename it "Super.ROM" for BIOS recovery use.

2. Insert the USB device that contains the new BIOS image ("Super.ROM") into your USB drive and power on the system

3. While powering on the system, please keep pressing <Ctrl> and <Home> simultaneously on your keyboard *until the following screen (or a screen similar to the one below) displays*.

Warning: Please **stop** pressing the <Ctrl> and <Home> keys immediately when you see the screen (or a similar screen) below; otherwise, it will trigger a system reboot.



Note: On the other hand, if the following screen displays, please load the "Super.ROM" file to the root folder and connect this folder to the system. (You can do so by inserting a USB device that contains the new "Super.ROM" image to your machine for BIOS recovery.)



4. After locating the new BIOS binary image, the system will enter the BIOS Recovery menu as shown below.



Note: At this point, you may decide if you want to start the BIOS recovery. If you decide to proceed with BIOS recovery, follow the procedures below.

5. When the screen as shown above displays, use the arrow keys to select the item "Proceed with flash update" and press the <Enter> key. You will see the BIOS recovery progress as shown in the screen below.

Note: *Do not interrupt the BIOS flashing process until it has completed*.



6. After the BIOS recovery process is completed, press any key to reboot the system.



7. Using a different system, extract the BIOS package into a USB flash drive.

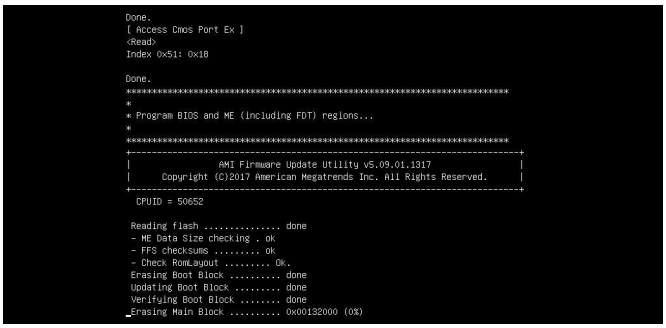
8. Press <Del> during system boot to enter the BIOS setup utility. From the top of the tool bar, click on Boot and press <Enter> to enter the submenu. From the submenu list, select Boot Option #1 as shown below. Then, boot Option #1 to [UEFI AP:UEFI: Built-in EFI Shell]. Press <F4> to save the settings and exit the BIOS setup utility.



9. When the UEFI Shell prompt appears, type fs# to change the device directory path. Go to the directory that contains the BIOS package you extracted earlier from Step 7. Enter flash.nsh BIOSname.### at the prompt to start the BIOS update process.



Note: *Do not interrupt this process* until the BIOS flashing is complete.



10. When the UEFThe screen above indicates that the BIOS update process is completed. When you see the screen above, unplug the AC power cable from the power supply, clear CMOS, and plug the AC power cable in the power supply again to power on the system.



11. Press <Del> to enter the BIOS setup utility.

12. Press <F3> to load the default settings.

13. After loading the default settings, press <F4> to save the settings and exit the BIOS setup utility.